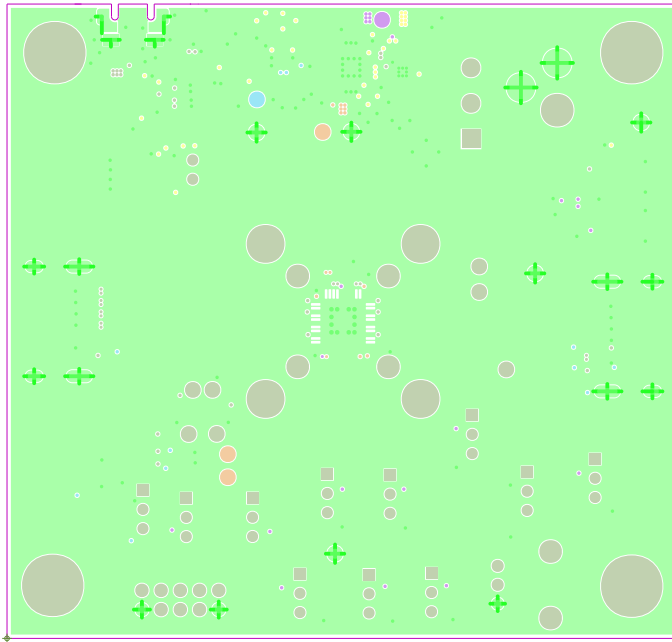
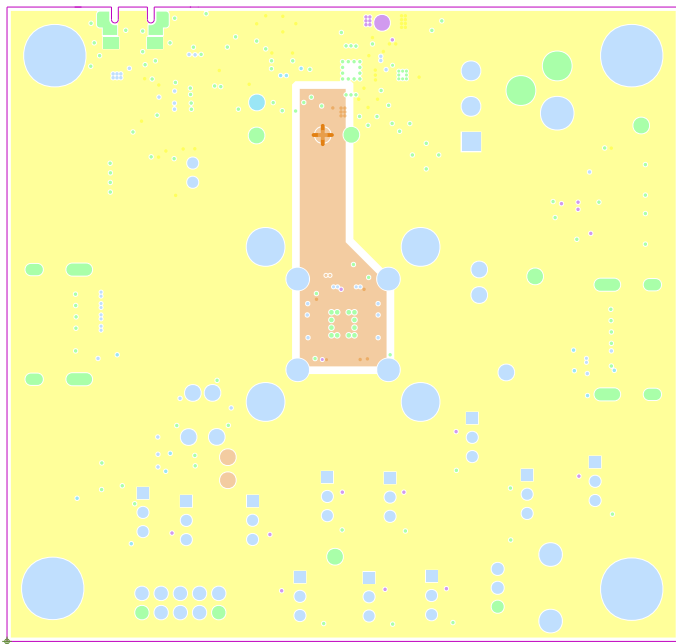


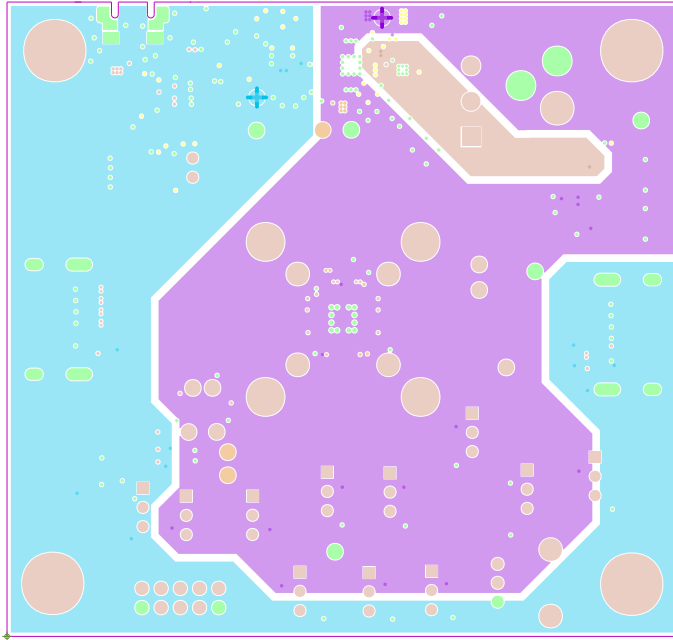
BOARD NAME: TMS181RGZEVm REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 1 - TOP	



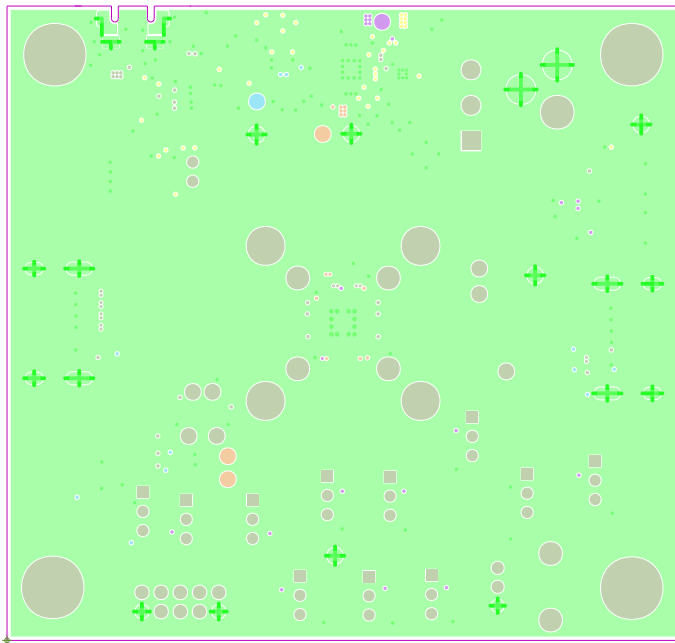
BOARD NAME: TMDS181RGZEVN REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 2 - GND	



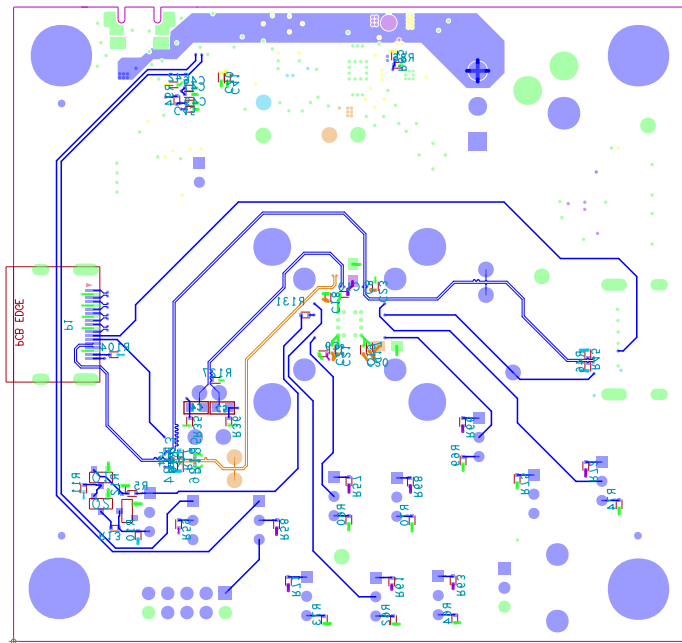
BOARD NAME: TMDS181RGZEVN REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 3 - PWR	



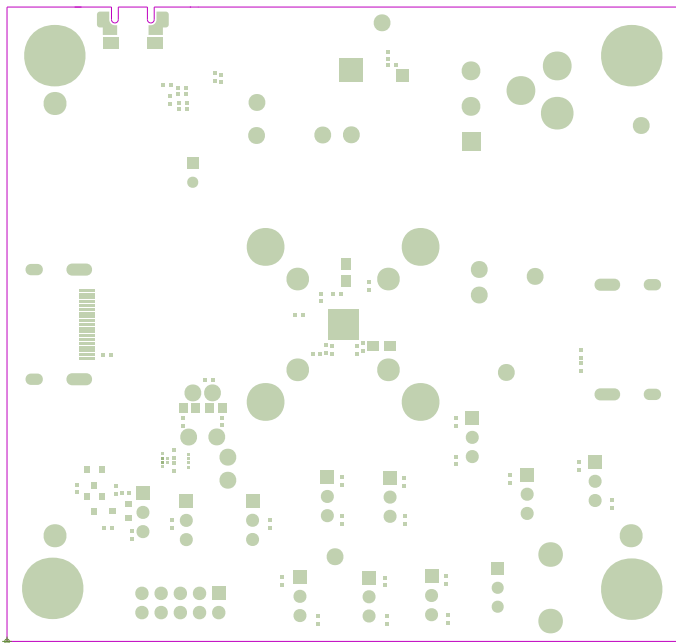
BOARD NAME: TMDS181RGZEVN REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 4 - PWR	



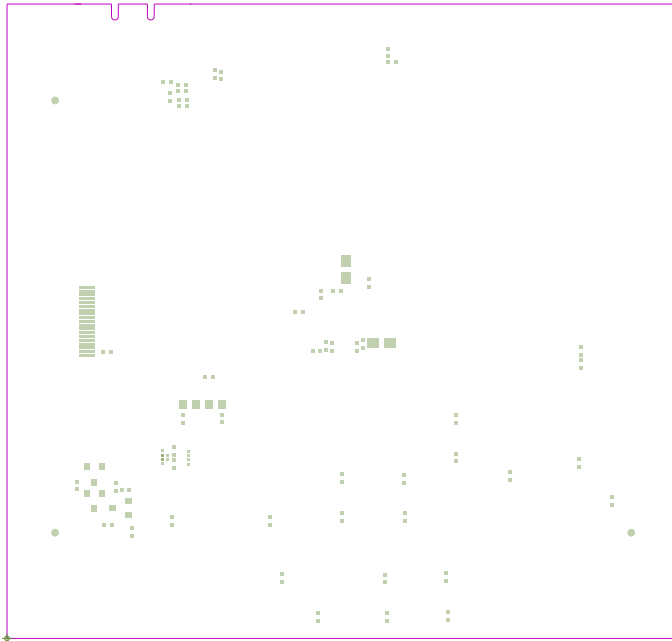
BOARD NAME: TMS181RGZEVN REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 5 - GND	



BOARD NAME: TMDS181RGZEVN REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 6 - BOTTOM	

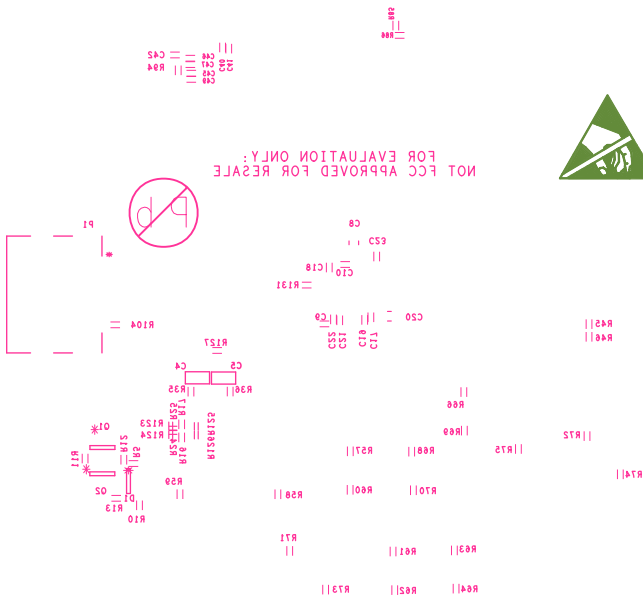


BOARD NAME: TMDS181RGZEVN REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 8 - SOLDERMASK BOTTOM	

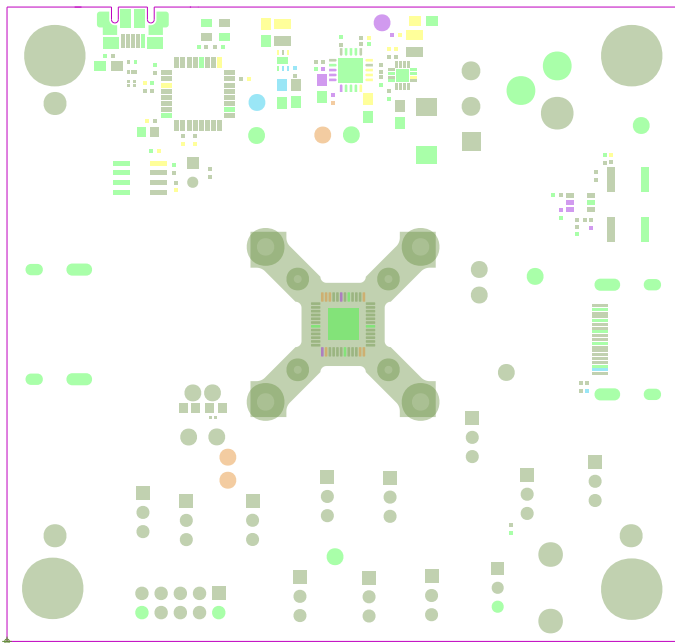


BOARD NAME: TMDS181RGZEVN REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 10 - SOLDERPASTE BOTTOM	

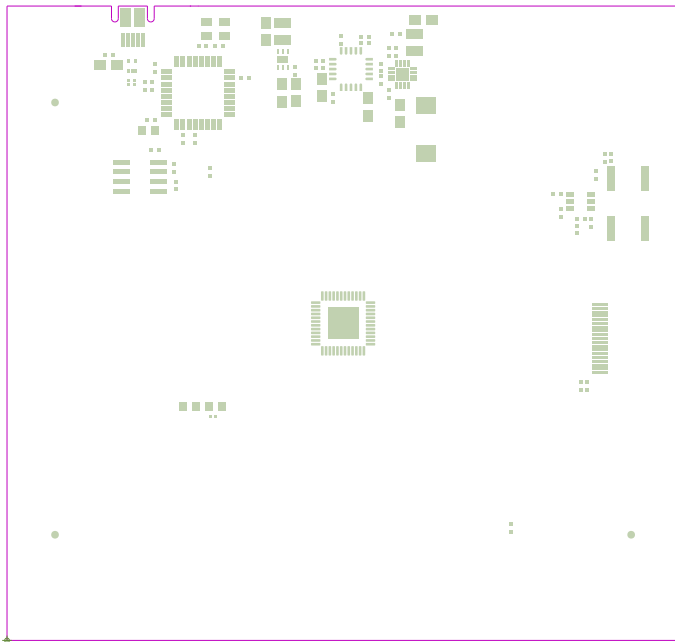
NOT FCC APPROVED FOR RESALE
FOR EVALUATION ONLY:



BOARD NAME: TMSD181RGZEV REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 12 - SILKSCREEN BOTTOM	

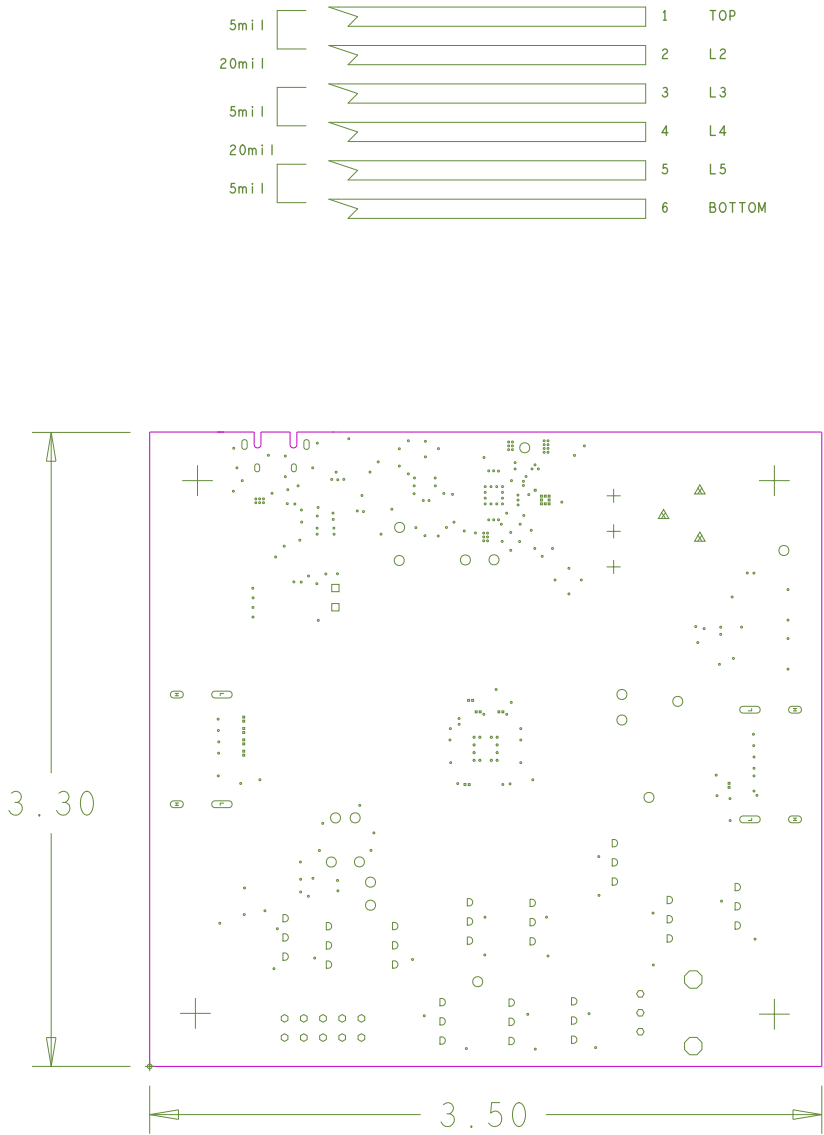


BOARD NAME: TMDS181RGZEVN REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 7 - SOLDERMASK TOP	



BOARD NAME: TMS181RGZEV REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 9 - SOLDERPASTE TOP	

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
+	160.0	+3.0/-3.0	PLATED	4
▲	140.0	+2.0/-2.0	PLATED	1
▲	120.0	+2.0/-2.0	PLATED	2
○	98.0	+3.0/-3.0	PLATED	2
	90.55	+3.0/-3.0	PLATED	4
+	73.0	+3.0/-3.0	PLATED	3
○	52.0	+3.0/-3.0	PLATED	17
◦	40.0	+3.0/-3.0	PLATED	10
◦	39.0	+3.0/-3.0	PLATED	3
▣	38.0	+3.0/-3.0	PLATED	2
◦	38.0	+3.0/-3.0	PLATED	33
•	12.0	+3.0/-12.0	PLATED	12
•	8.0	+3.0/-3.0	PLATED	262
	41.34	+3.0/-3.0	NON-PLATED	4
⊖	106.3x35.43	+1.97/-1.97	PLATED	4
⊖	66.93x35.43	+3.15/-3.15	PLATED	4
◦	51.18x27.56	+3.15/-3.15	PLATED	2
◦	41.34x25.59	+3.15/-3.15	PLATED	2



- GENERAL NOTES. UNLESS OTHERWISE SPECIFIED
1. ALL FABRICATION ITEMS MUST MEET OR EXCEED BEST
 2. INDUSTRY PRACTICE : IPC-A-600C (Commercial Std.)
 3. LAMINATE MATERIAL : FR4 POLYCLAD 370 or EQUIVALENT
 4. COPPER WEIGHT : 1oz START INTERNAL 1/2oz START EXTERNAL
 5. FINISHED BOARD THICKNESS : BE .062 +/- .10%
 6. MAXIMUM WARP AND TWIST TO BE .005 INCH PER INCH
 7. MINIMUM COPPER WALL THICKNESS OF PLATED-THRU HOLES TO BE .001 INCH THERE MUST BE .01MIL OR GREATER PLATING ON EACH SIDE OF THE BARREL WHEN VIEWED IN THE CROSS SECTION
 8. MINIMUM ANNULAR RING OF PLATED-THRU HOLES TO BE .002 INCH
 9. MAXIMUM ALLOWABLE LINE REDUCTION TO BE 20% OR .002 WHICHEVER IS SMALLER
 10. 0.005 SIGNAL LINES ON LAYER 1 & 6 TO BE IMPEDANCE CONTROLLED 100 OHMS DIFFERENTIAL +/- 5%
 11. 0.008 SIGNAL LINES ON LAYER 1 & 6 TO BE IMPEDANCE CONTROLLED 50 OHMS +/- 5%
 12. DIELECTRIC CONSTANTS ARE CORE : 4.1 PREPREG : 4.1
 13. A CROSS SECTION MOUNT AND DIGITAL PHOTO'S OF VIA'S TO BE PROVIDED
- PROCESS NOTES
1. CIRCUITRY ON OUTER LAYERS TO BE PLATED WITH : NICKEL GOLD
 2. SOLDERMASK BOTH SIDES PER ARTWORK : GREEN LPI
 3. SILKSCREEN BOTH SIDES PER ARTWORK : COLOR = WHITE

BOARD NAME: TMD5181RGZEVm REV A	BOARD REV: 1.0	KSID: 16027	JOB NUMBER: 126296
ALL ARTWORK LAYERS VIEWED FROM TOP		LAYER DESCRIPTION: LAYER 13 - FAB	