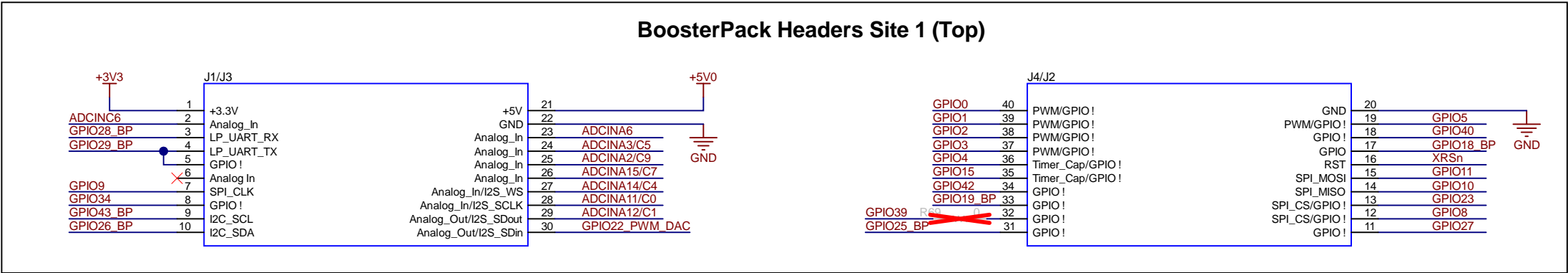


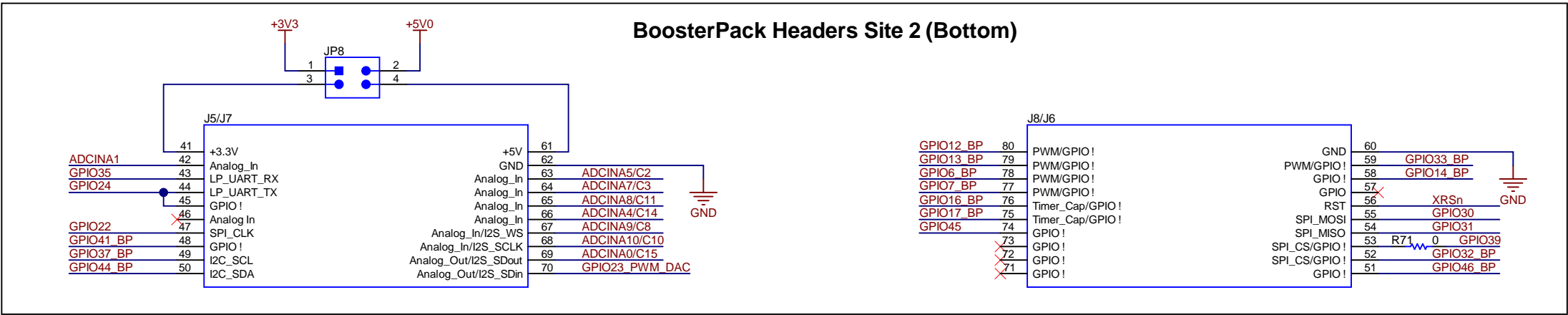
A

A



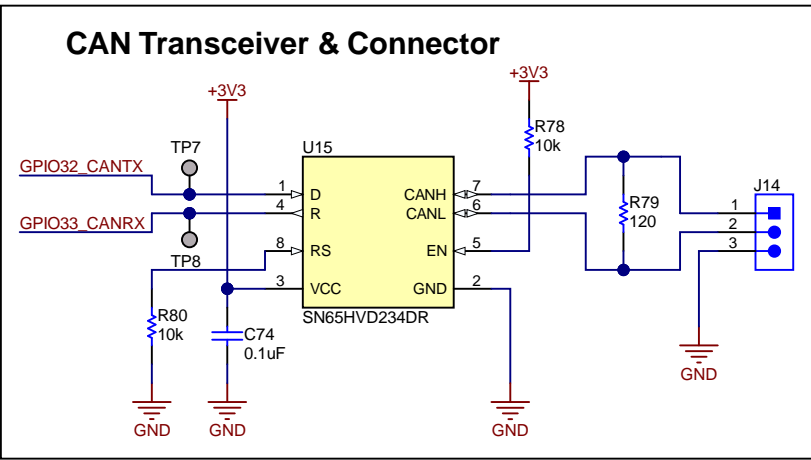
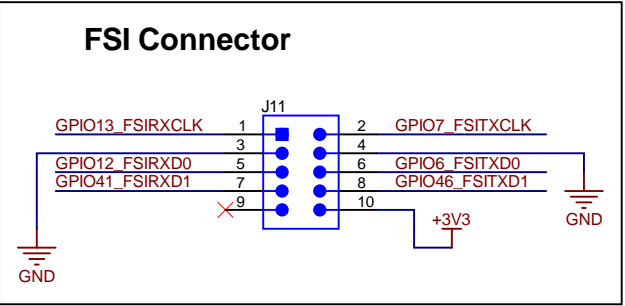
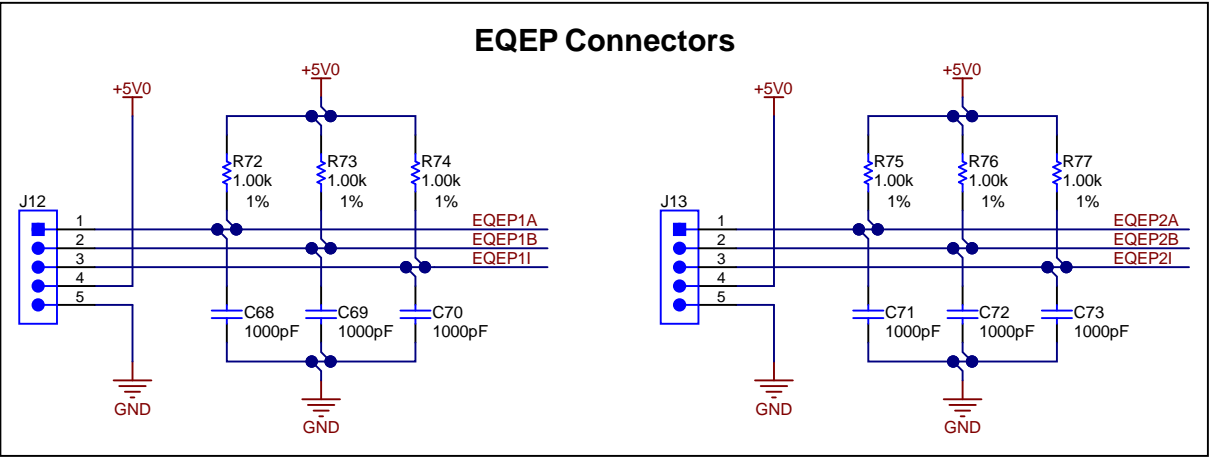
B

B



C

C



D

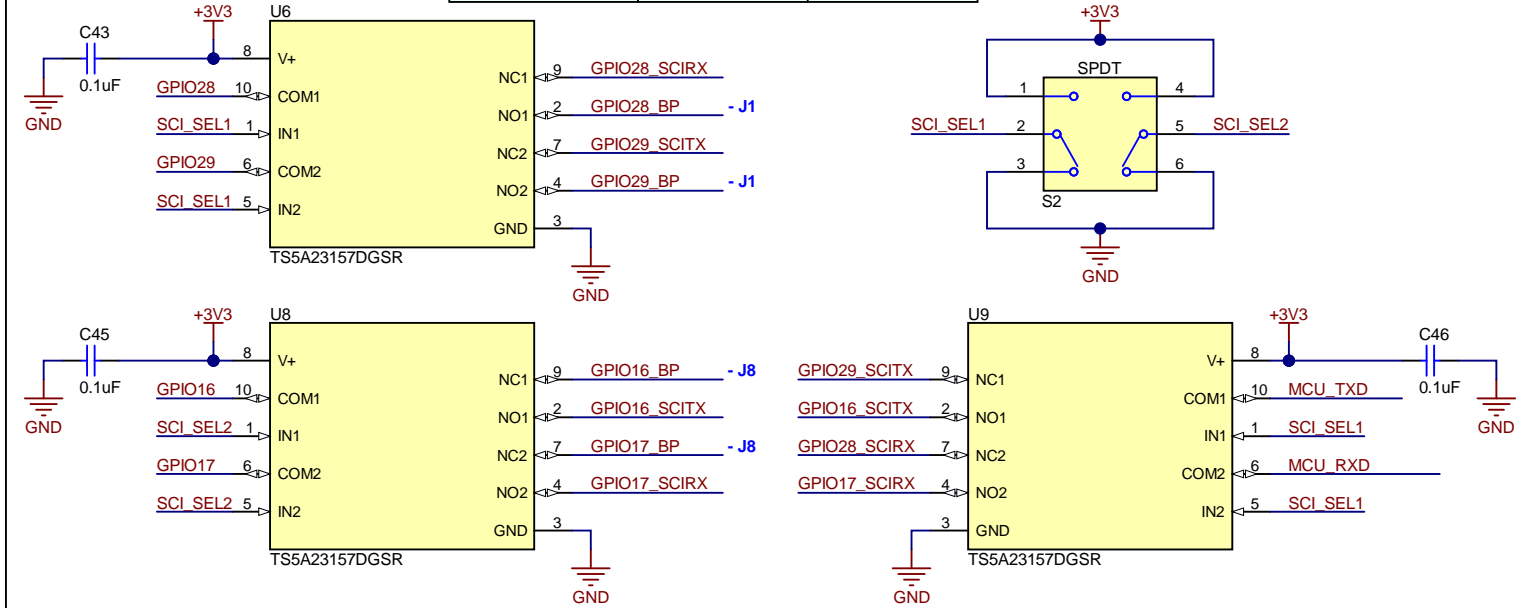
D

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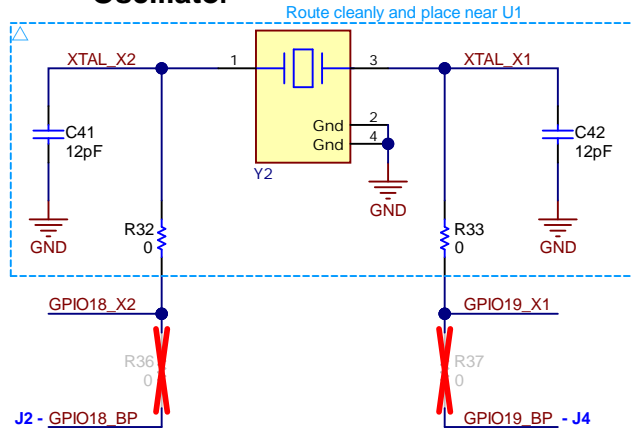
UART Routing

SCI_SEL1	SCI_SEL2	GPIO28/29 Route	GPIO16/17 Route
0	0	XDS110 COM Port	BP
0	1	XDS110 COM Port	NC
1	0	BP	BP
1	1	BP	XDS110 COM Port

- DEFAULT



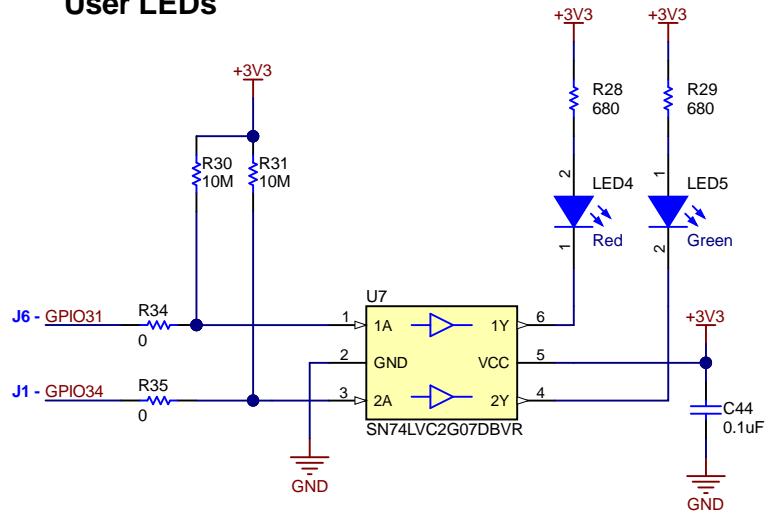
Oscillator



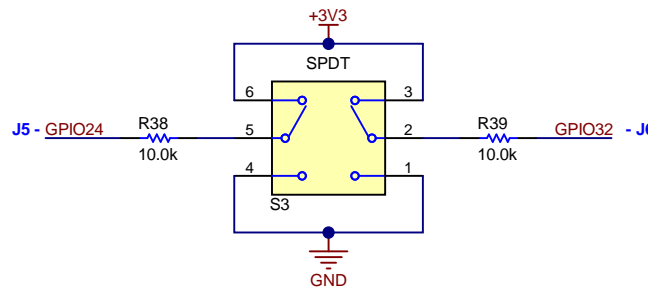
By default:
- Crystal Y2 is connected between GPIO18_X2 and GPIO19_X1.
- GPIO18_BP AND GPIO19_BP are connected to the BoosterPack headers.

If GPIO18 and GPIO 19 are needed at the BoosterPack Headers:
- Remove R32 and R33, populate R36 and R37 with 0 ohm resistors
- The F28002x device's internal oscillator will need to be used

User LEDs



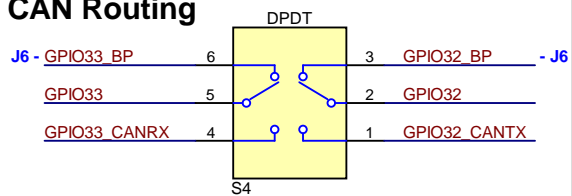
Boot Mode Select



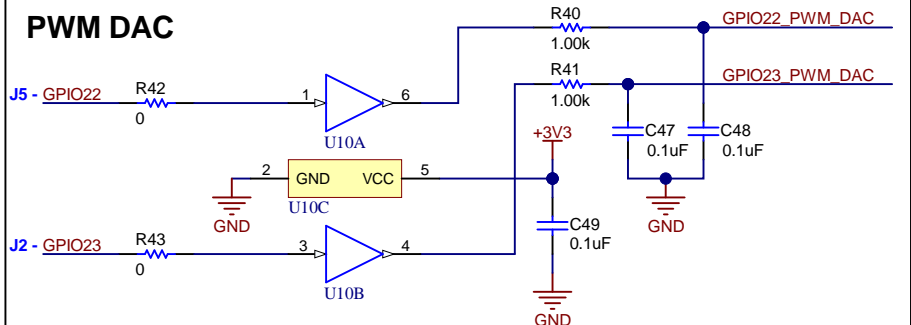
Selected Boot Mode Chart

Mode #	GPIO24	GPIO32	Boot Mode
00	0	0	Boot from Parallel GPIO
01	0	1	Boot from SCI / Wait Mode
02	1	0	Boot from CAN
03	1	1	Boot from Flash

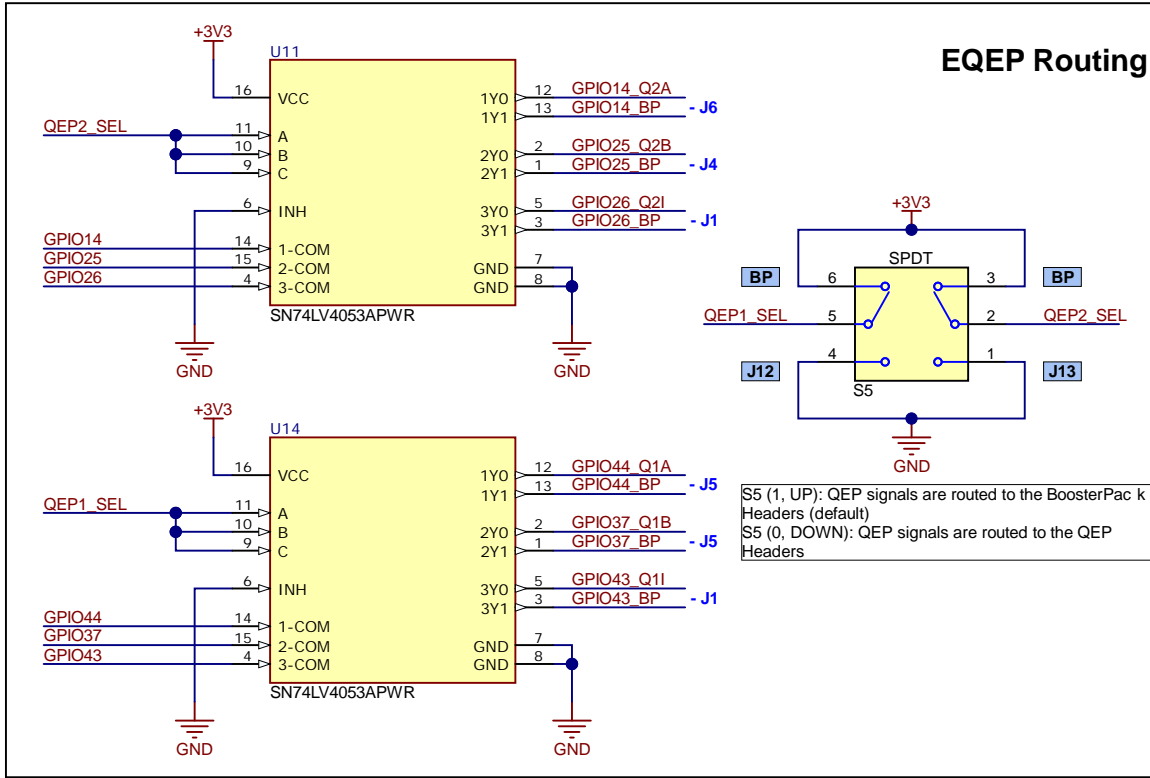
CAN Routing



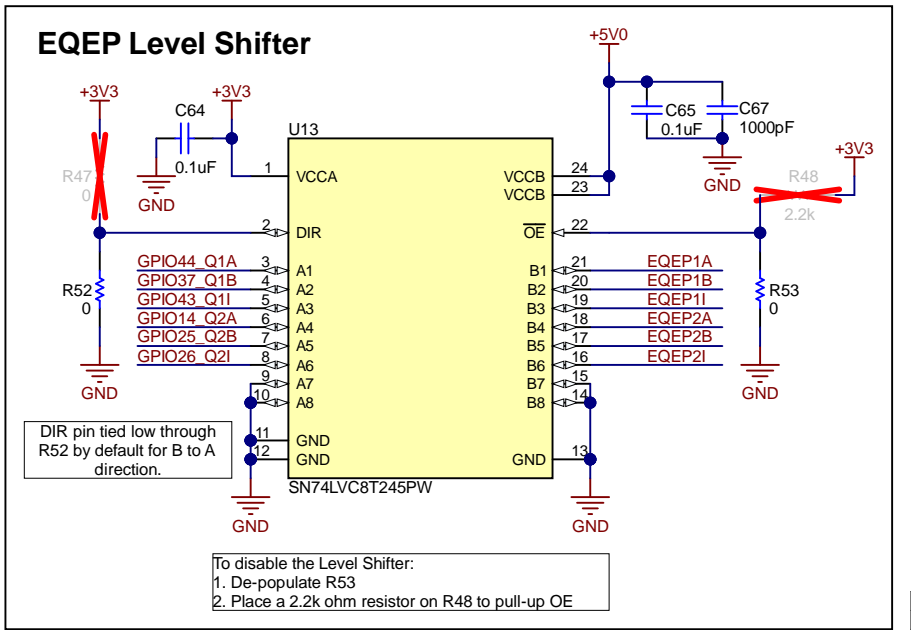
PWM DAC



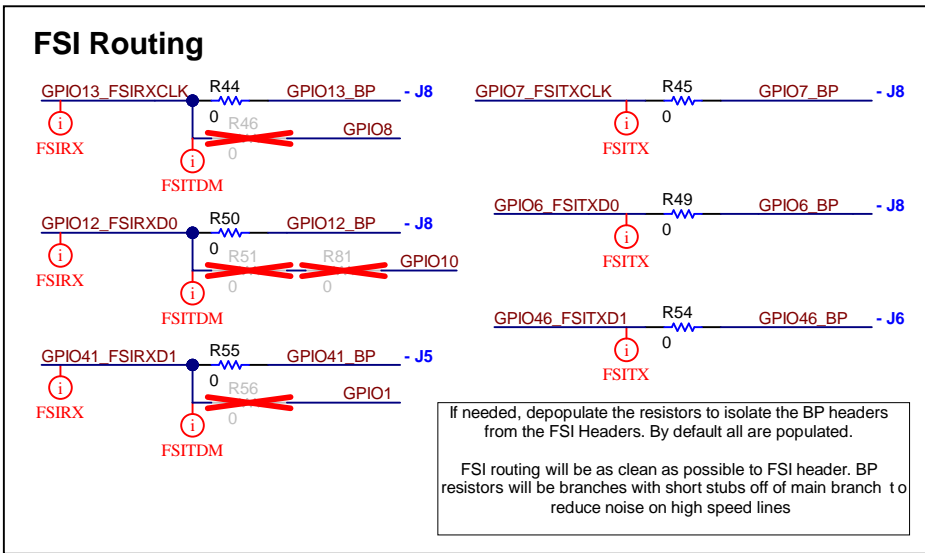
EQEP Routing



EQEP Level Shifter



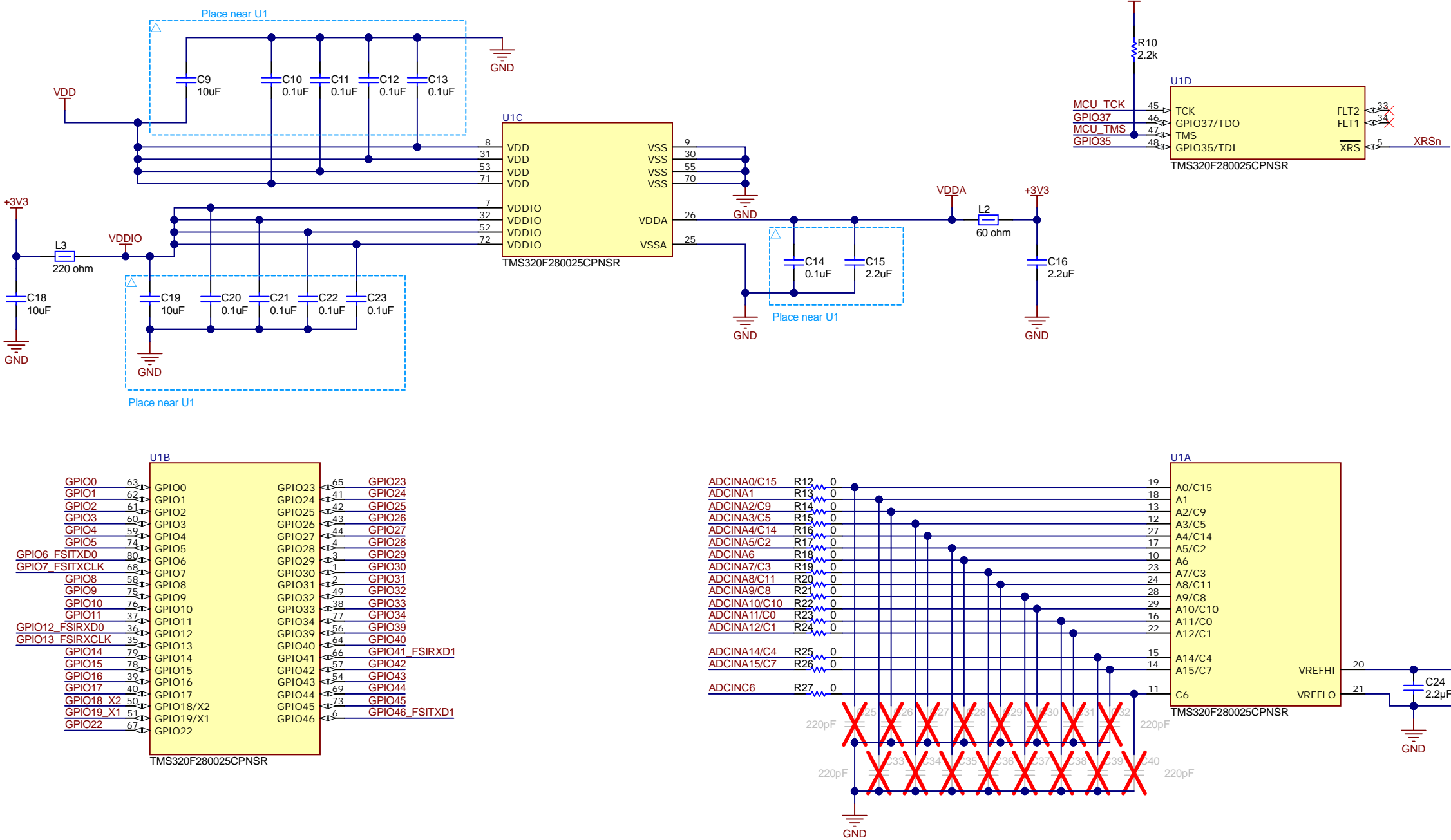
FSI Routing



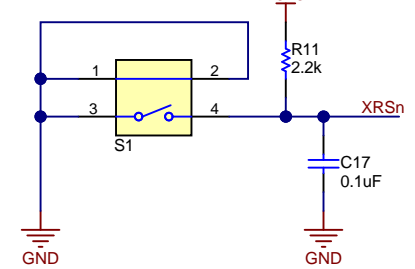
If needed, depopulate the resistors to isolate the BP headers from the FSI Headers. By default all are populated.
FSI routing will be as clean as possible to FSI header. BP resistors will be branches with short stubs off of main branch to reduce noise on high speed lines

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F28002x Device



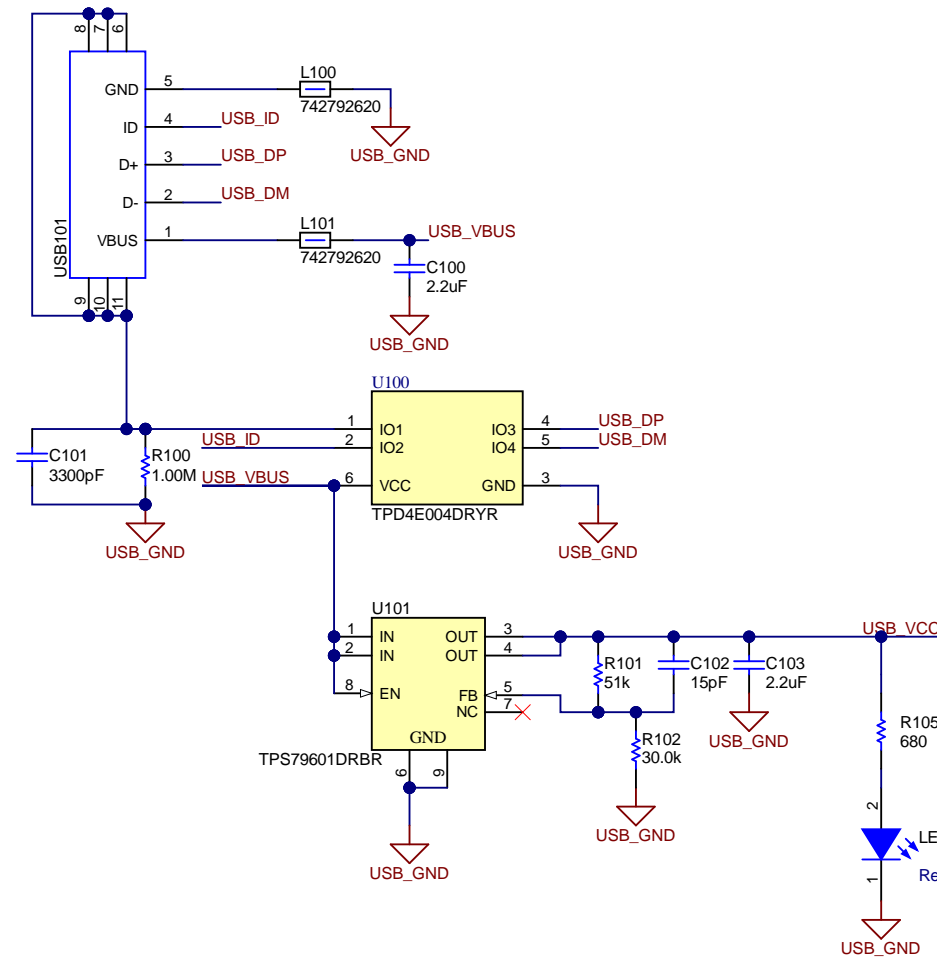
Reset Button



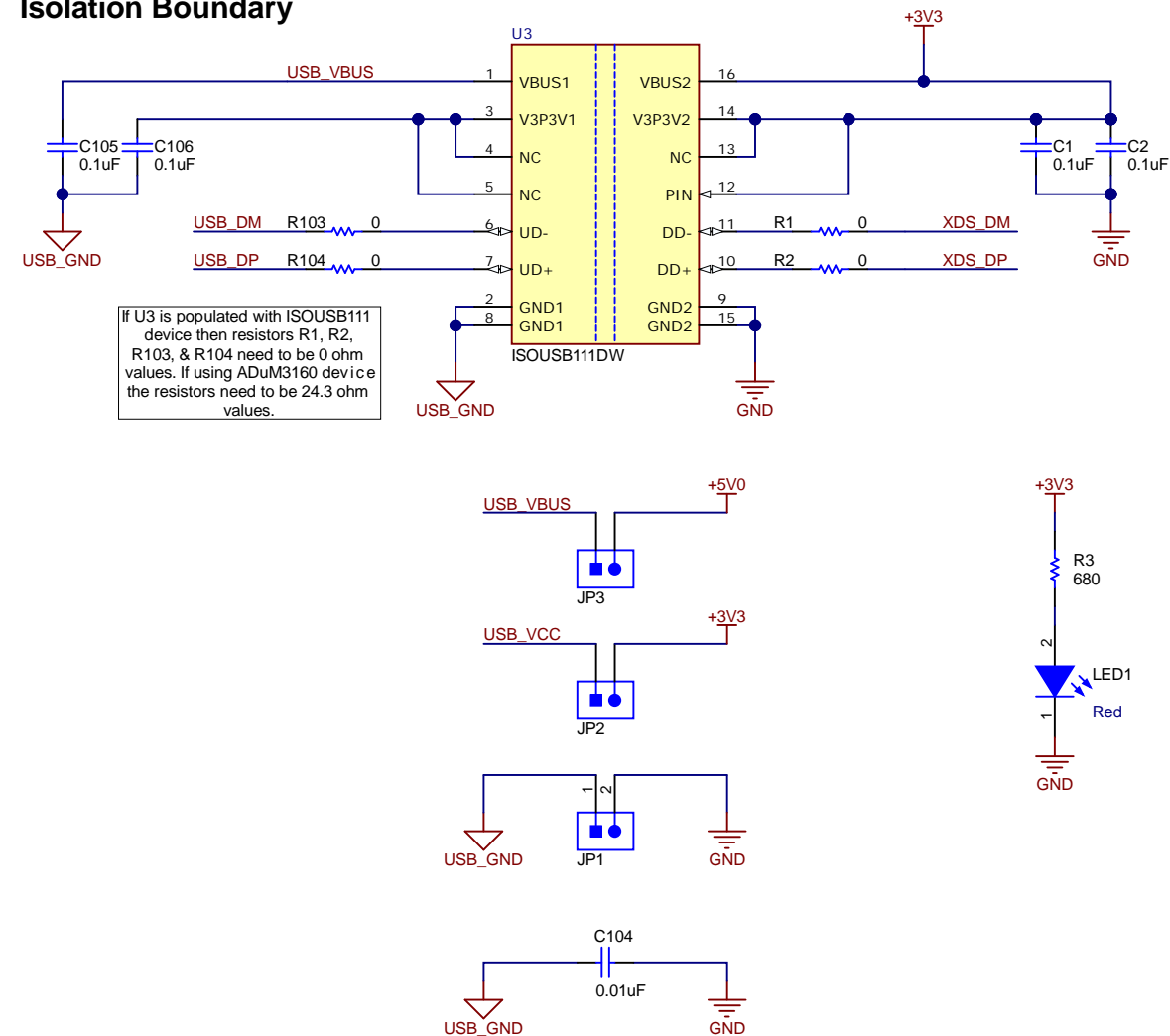
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Orderable: LAUNCHXL-F280025C	Designed for: Public Release	Mod. Date: 2/2/2022
TID #: N/A	Project Title: LAUNCHXL-F280025C	
Number: MCU089	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 8
Drawn By:	File: MCU089B_F280025C_Device.SchDoc	Size: B
Engineer: Kevin Allen	Contact: http://www.ti.com/support	

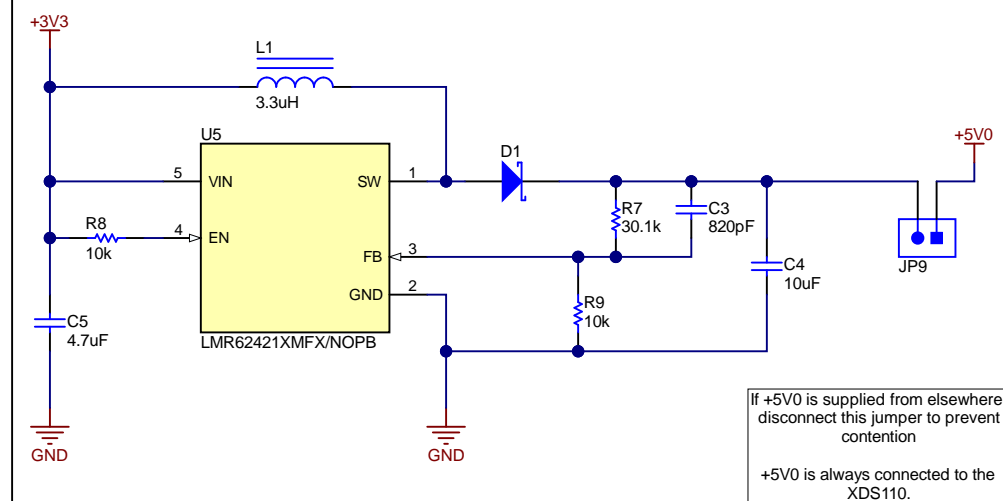
USB & XDS Power



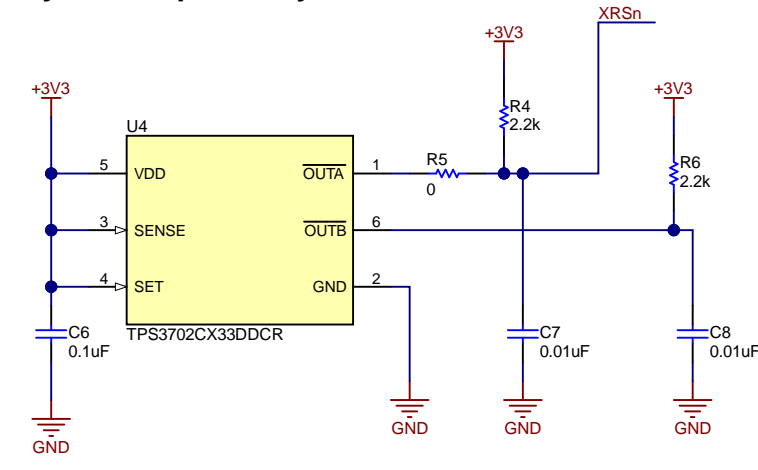
Isolation Boundary



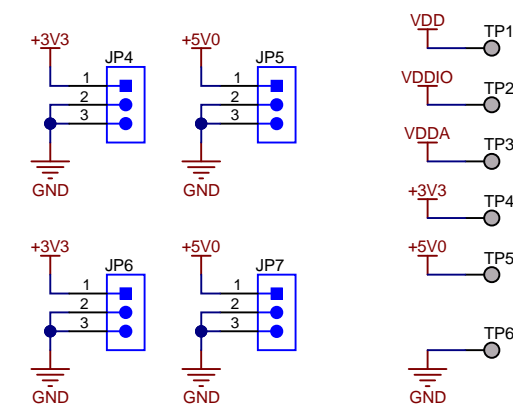
3.3V to 5V Boost

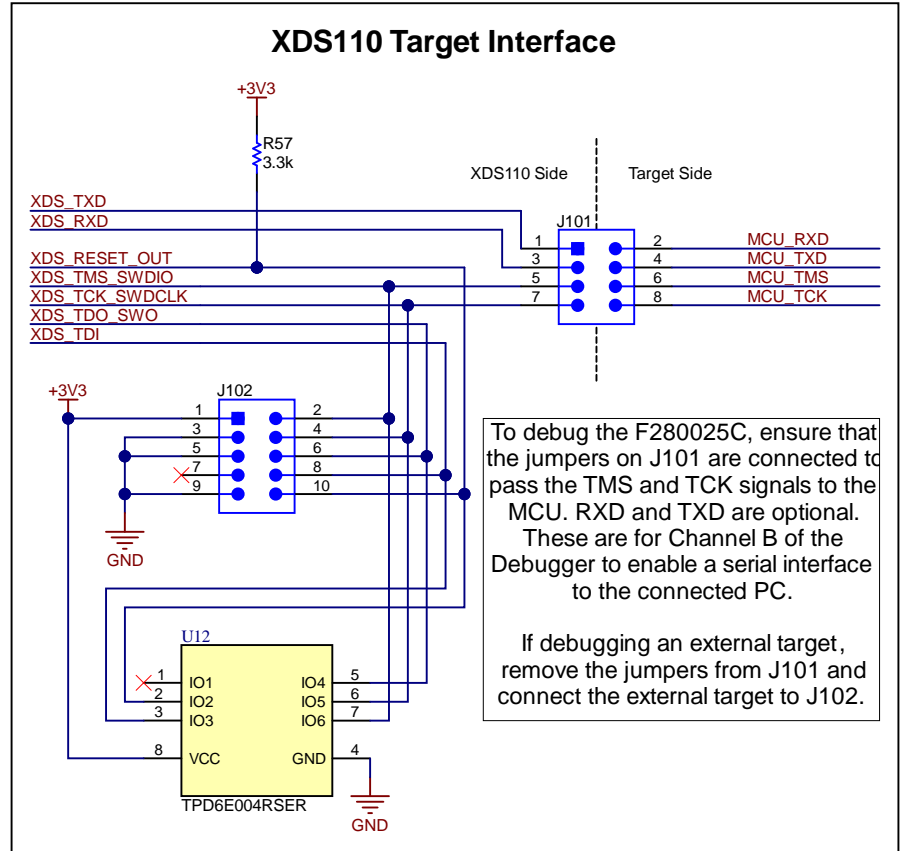
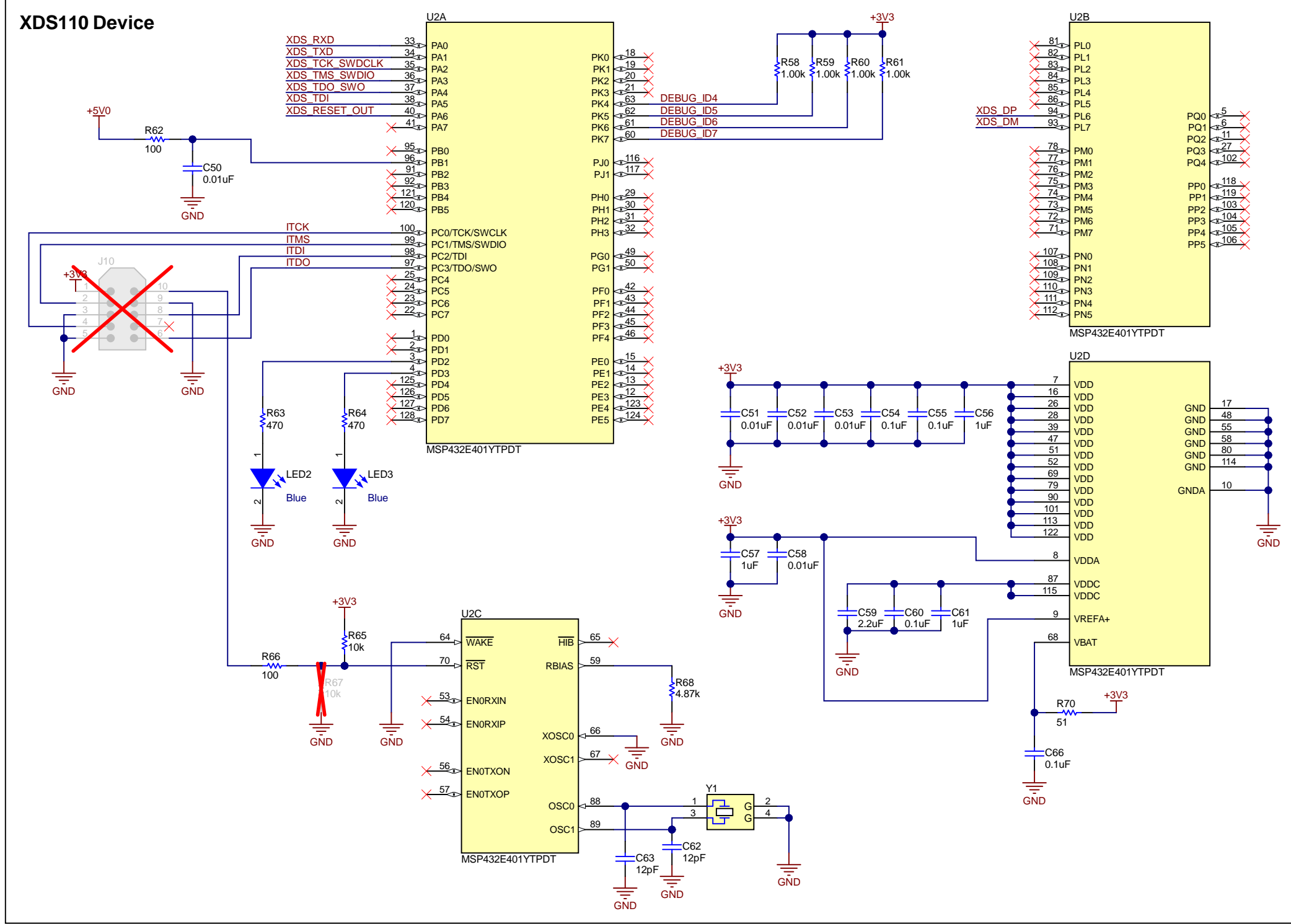


System Supervisory Circuit



Power Headers and Test Points





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A

A



PCB Number: MCU089
PCB Rev: B

Logo1
PCB
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Logo3
PCB
LOGO
FCC disclaimer

Logo4
PCB
LOGO
WEEE logo

Logo5
PCB
LOGO
Texas Instruments



ZZ1

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed

ZZ2


Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: LAUNCHXL-F280025C	Designed for: Public Release	Mod. Date: 2/3/2022	
TID #: N/A	Project Title: LAUNCHXL-F280025C		
Number: MCU089	Rev: B	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 8	
Drawn By:	File: MCU089B_Hardware.SchDoc	Size: B	
Engineer: Kevin Allen	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2022



D

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