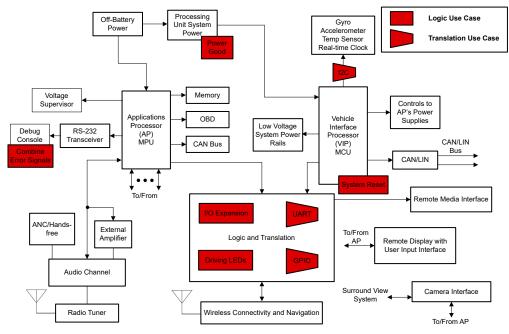
Application Brief **Optimizing Digital Cockpit Processing Units (DCPUs) Using Logic and Translation**



Albert Xu

Functional Block Diagram

Simplified Block Diagram for Digital Cockpit Processing Unit (DCPU) shows a simplified DCPU to illustrate the logic and translation use cases. Each red block has an associated use-case document; links are provided in Table 1 and Table 2. For a more complete block diagram, see the interactive online end equipment reference diagram for DCPUs.



Simplified Block Diagram for Digital Cockpit Processing Unit

Logic and Translation Use Cases

Each use case links to a separate document that provides additional details including a block diagram, design tips, and part recommendations. The nearest block and use-case identifiers are listed to match to the use cases shown in the provided *simplified block diagram*.

Nearest Block	Use-Case Identifier	Use Case				
Processing Unit System Power	Power Good Combination Combine Power Good Signals					
Debug Console	Fault Combination Use Fewer Inputs to Monitor Error Signals					
Logic and Translation	Driving LEDs Drive Indicator LEDs					
	I/O Expansion	Increase the Number of Outputs on a Microcontroller				

Table 1. Logic Use Cases

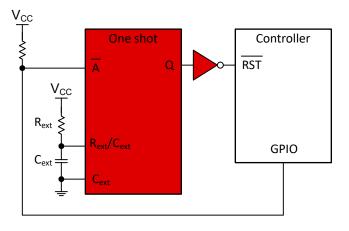
1



Table 2. Translation Use Cases						
Nearest Block	Use-Case Identifier	Use Case				
VIP MCU	12C	Translate Voltages for I2C				
Logic and Translation	UART	Translate Voltages for UART				
	GPIO	Translate Voltages for GPIO				

Reset a System for Short Time

System controllers can use GPIO pins to reset other components if a fault is detected; however, system controllers generally cannot reset themselves or their entire system. By using a monostable multivibrator, the system controller can reset the entire system.



Design Considerations

- Either falling-edge or rising-edge trigger configurations can be used for this application. See the data sheet of your selected multivibrator for details.
- A pullup or pulldown resistor is required to return the input signal to a valid state once the system controller turns off. The recommended value for this resistor is 10 kΩ.
- · Retriggerable or non-retriggerable monostable multivibrators can be used for this operation
- [FAQ] [H] Monostable Multivibrators Top Questions Answered
- [FAQ] How does a slow or floating input affect a CMOS device?
- [FAQ] Where do I find maximum power dissipation for a device?
- Ask a question on the *TI E2E*[™] forum

Table 3. Recommended Parts

Part Number	AEC-Q100	V _{CC} Range	Function	Features
SN74LVC1G123		1.65 V – 5.5 V	Single-channel multivibrator	Retriggerable, Schmitt-trigger inputs
SN74LV123A		2 V – 5.5 V	Dual-channel multivibrator	Retriggerable, Schmitt-trigger inputs, inverted output
SN74LV123A-Q1	√			
SN74LV221A		2 V – 5.5 V	Dual-channel multivibrator	Schmitt-trigger inputs, inverted output
SN74LV221A-Q1	√			
SN74AHC1G04		2 V – 5.5 V	Single-channel inverting buffer	
SN74AHC1G04-Q1	√			

For more devices, browse through the *online parametric tool* which allows users to sort by desired voltage, channel numbers, and other features.

2

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2023, Texas Instruments Incorporated