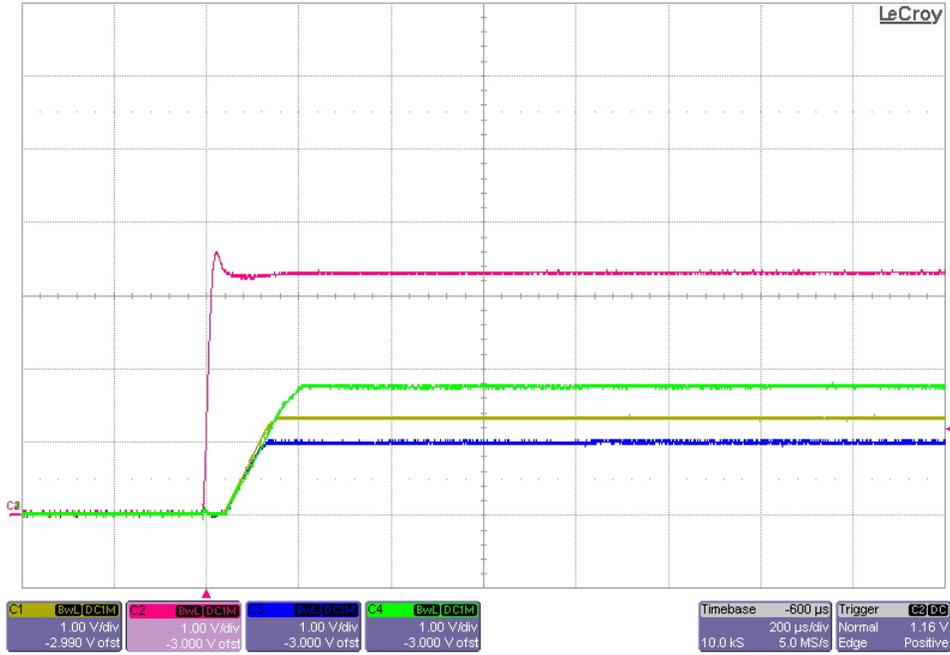
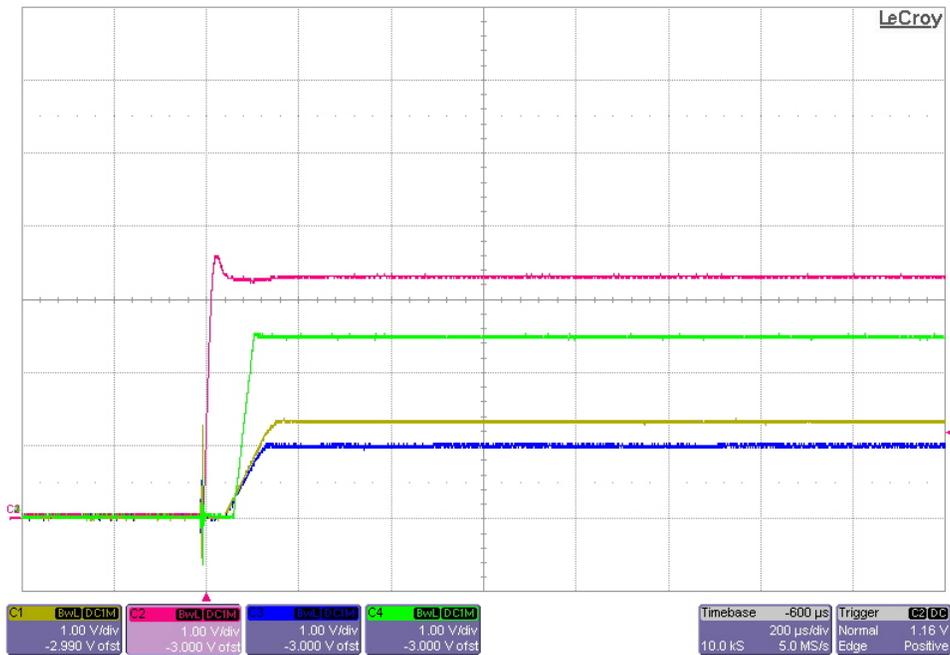


1 Startup

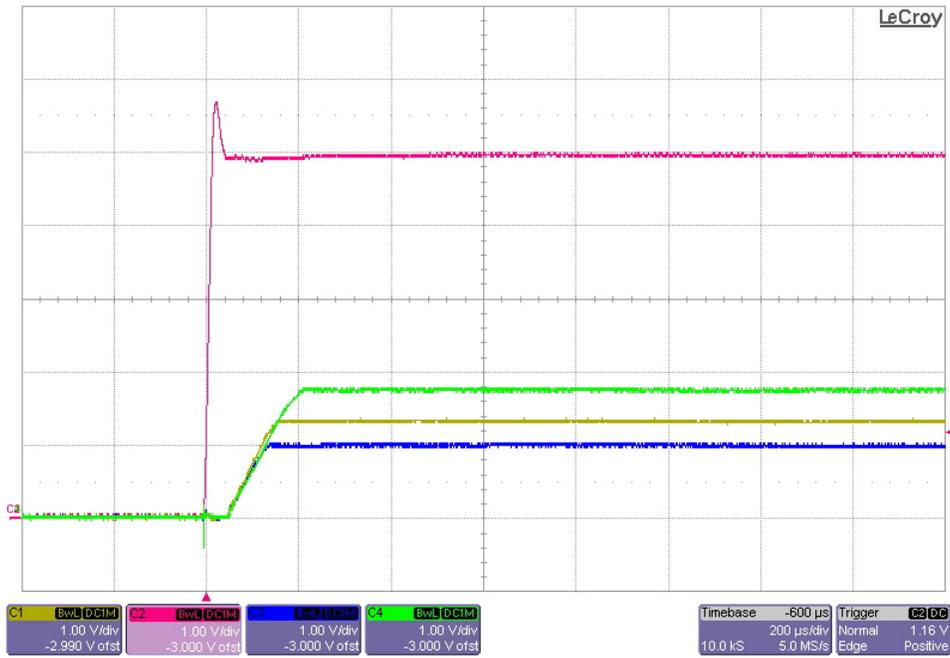
The photo below shows the output voltage startup waveforms (1V, 1.35V, 1.8V) after the application of 3.3V in. All outputs are unloaded. (1V/DIV, 200uS/DIV)



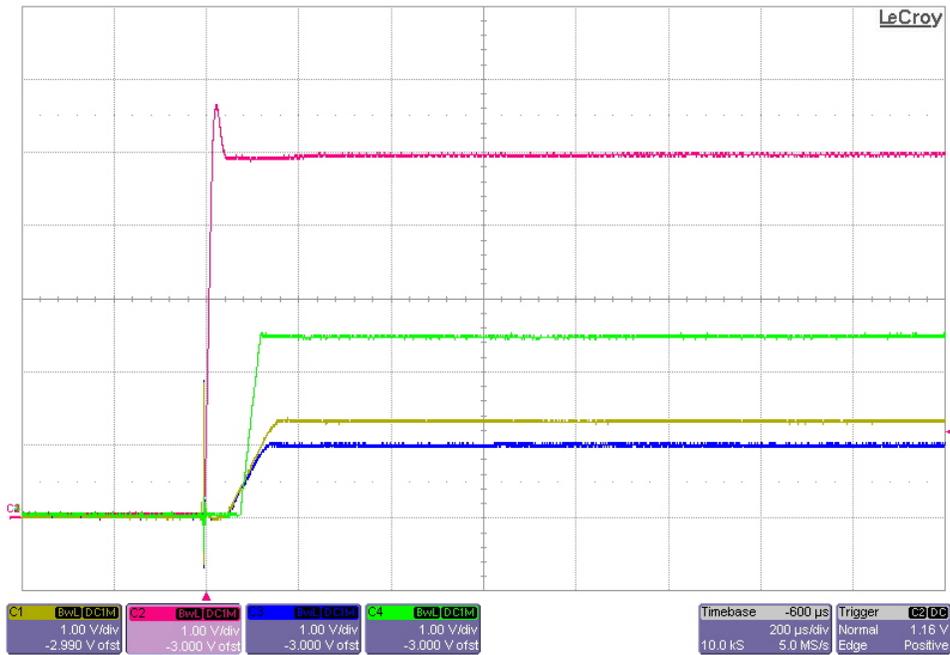
The photo below shows the output voltage startup waveforms (1V, 1.35V, 2.5V) after the application of 3.3V in. All outputs are unloaded. (1V/DIV, 200uS/DIV)



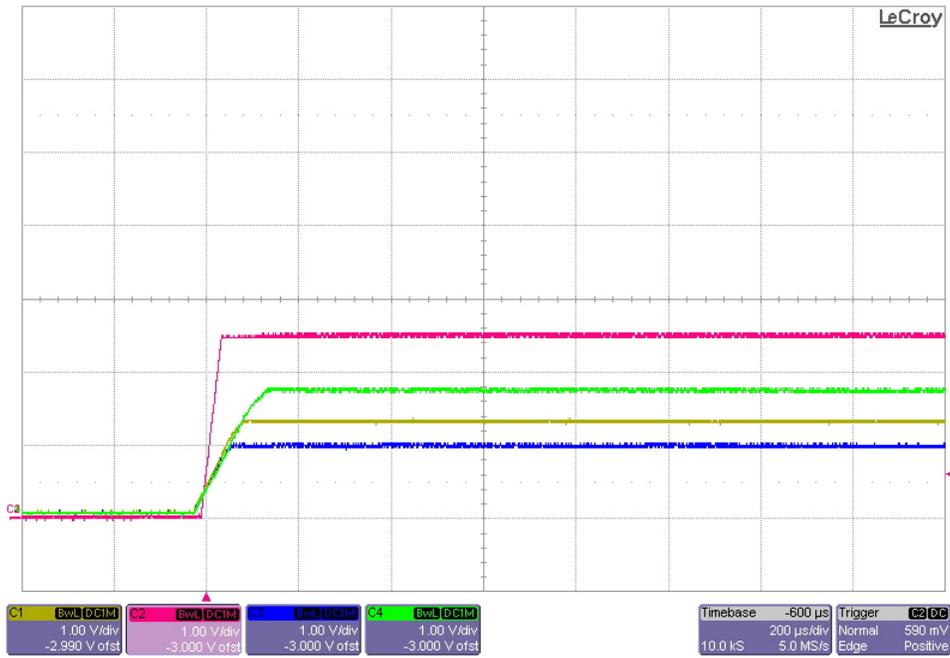
The photo below shows the output voltage startup waveforms (1V, 1.35V, 1.8V) after the application of 5V in. All outputs are unloaded. (1V/DIV, 200uS/DIV)



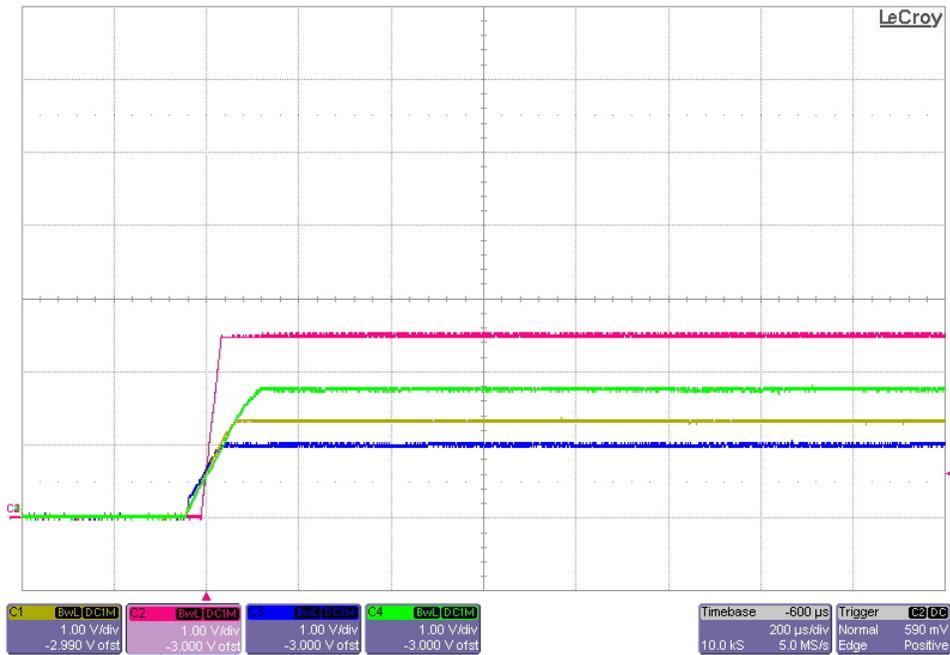
The photo below shows the output voltage startup waveforms (1V, 1.35V, 2.5V) after the application of 5V in. All outputs are unloaded. (1V/DIV, 200uS/DIV)



The photo below shows the output voltage startup waveforms (1V, 1.35V, 1.8V, 2.5V) after an ENABLE high is applied. The input voltage is 3.3V and all outputs are unloaded. (1V/DIV, 200uS/DIV)

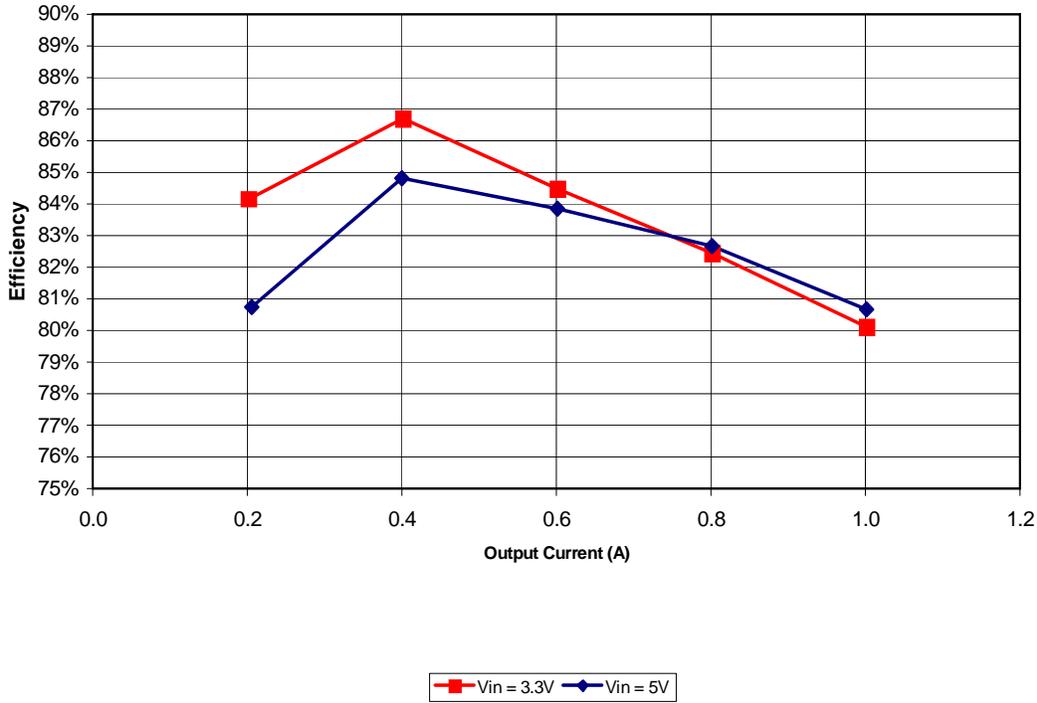


The photo below shows the output voltage startup waveforms (1V, 1.35V, 1.8V, 2.5V) after an ENABLE high is applied. The input voltage is 5V and all outputs are unloaded. (1V/DIV, 200uS/DIV)

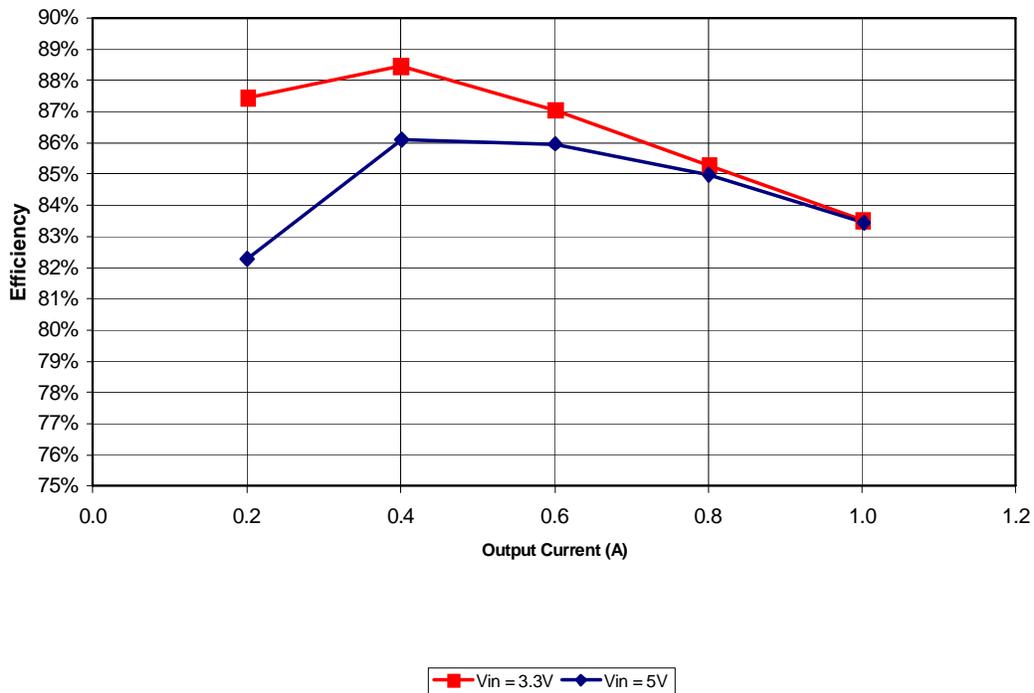


2 Efficiency

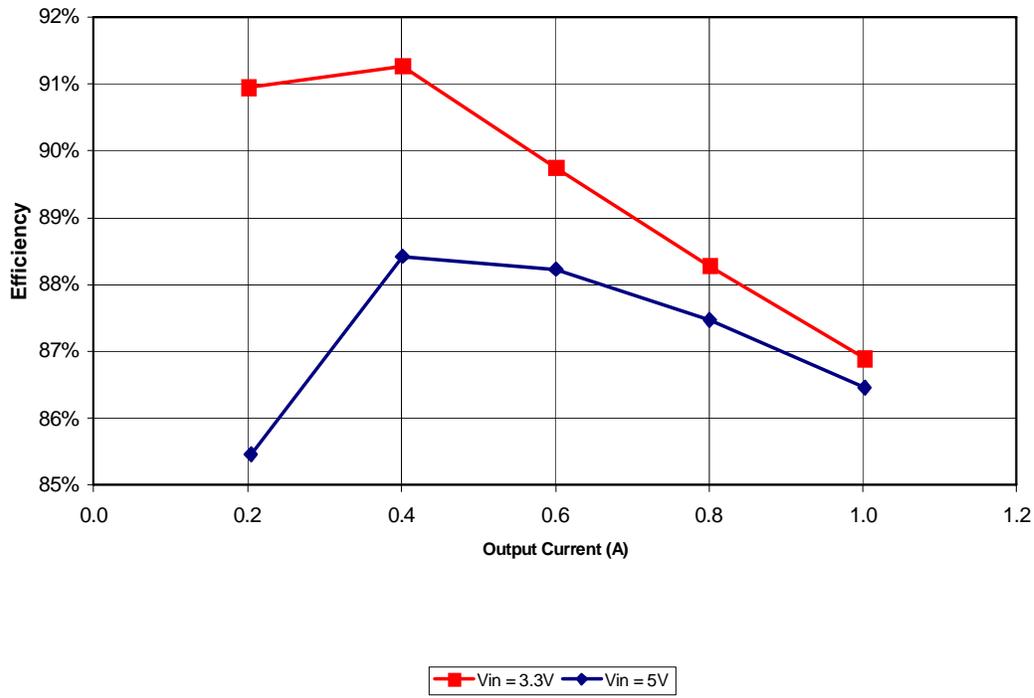
1V Converter Efficiency



1.35V Converter Efficiency

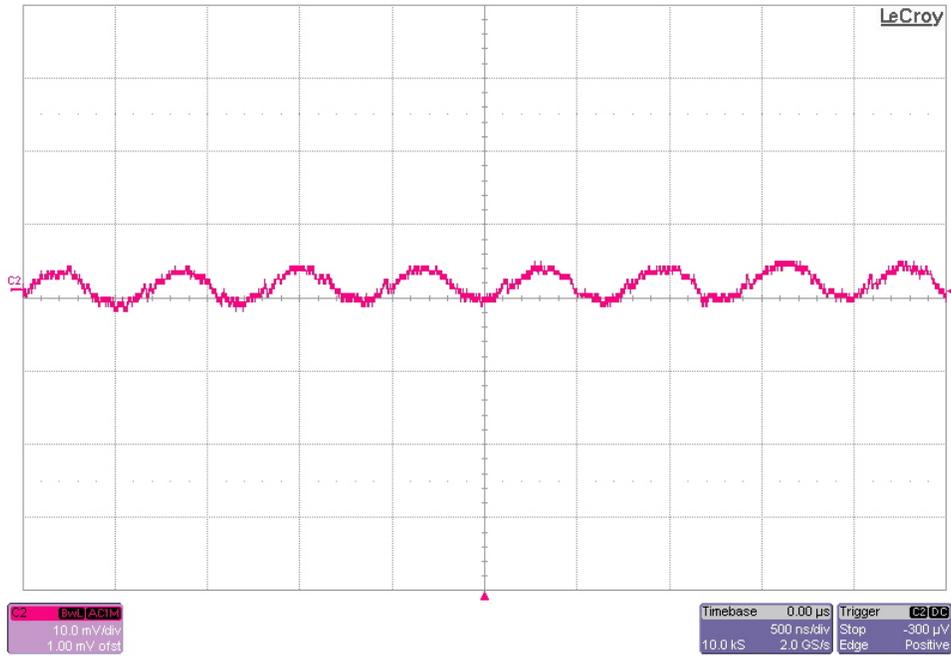


1.8V Converter Efficiency

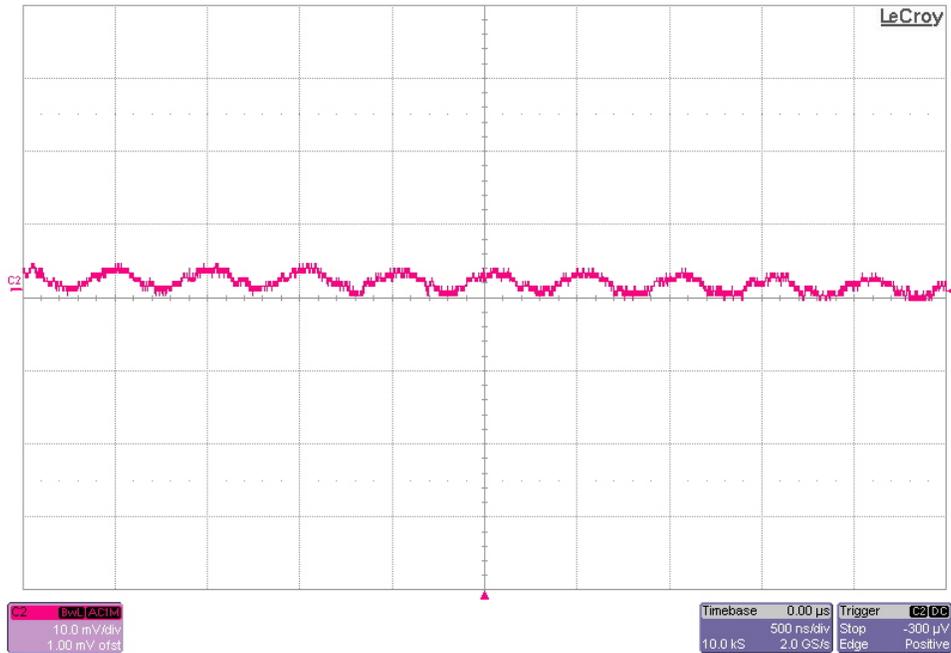


3 Output Ripple Voltage

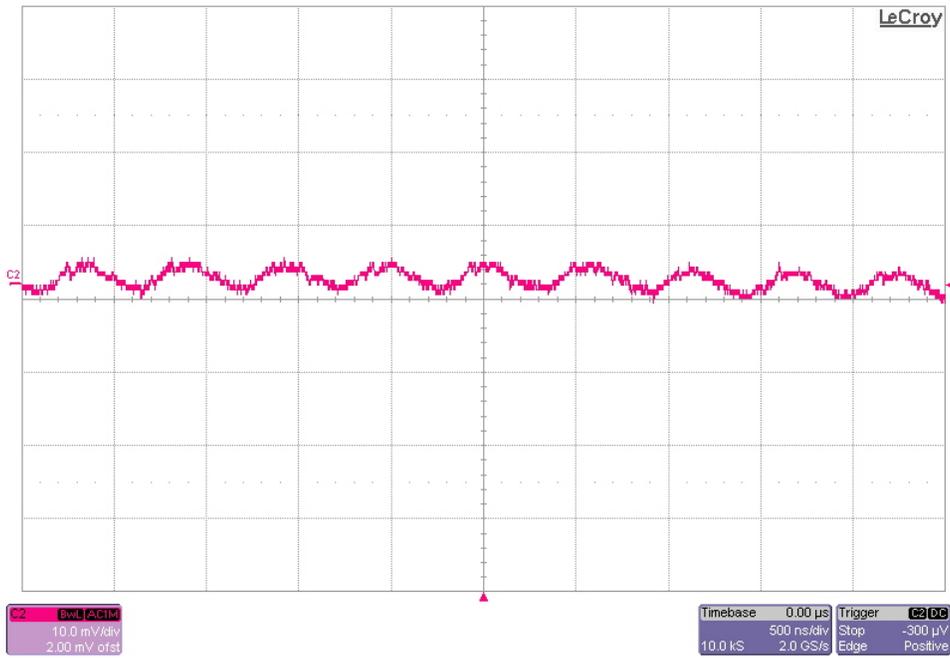
The 1V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 1A and the input voltage set to 3.3V. (10mV/DIV, 500nS/DIV)



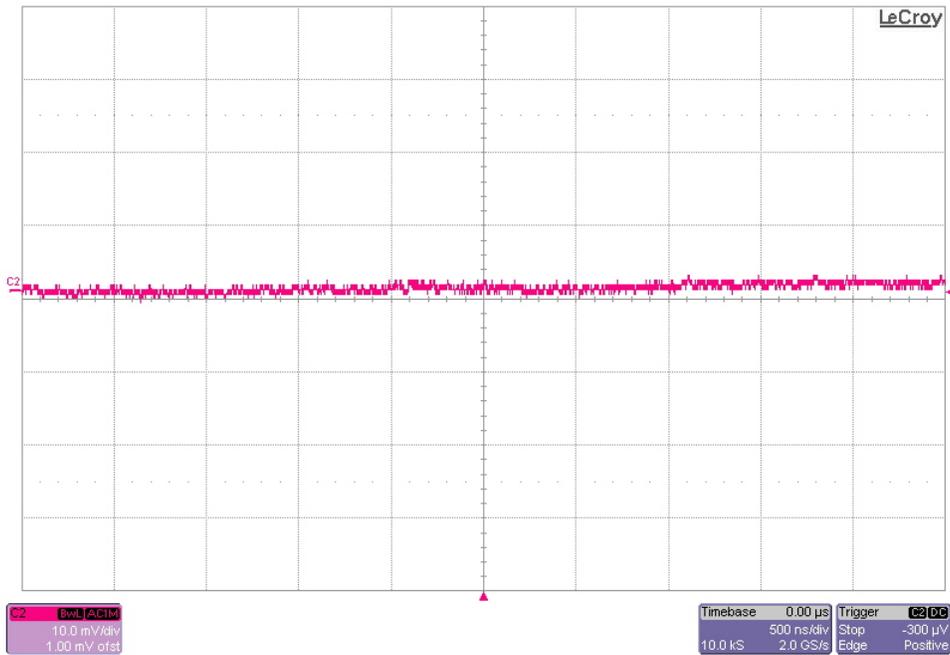
The 1.35V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 1A and the input voltage set to 3.3V. (10mV/DIV, 500nS/DIV)



The 1.8V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 1A and the input voltage set to 3.3V. (10mV/DIV, 500nS/DIV)

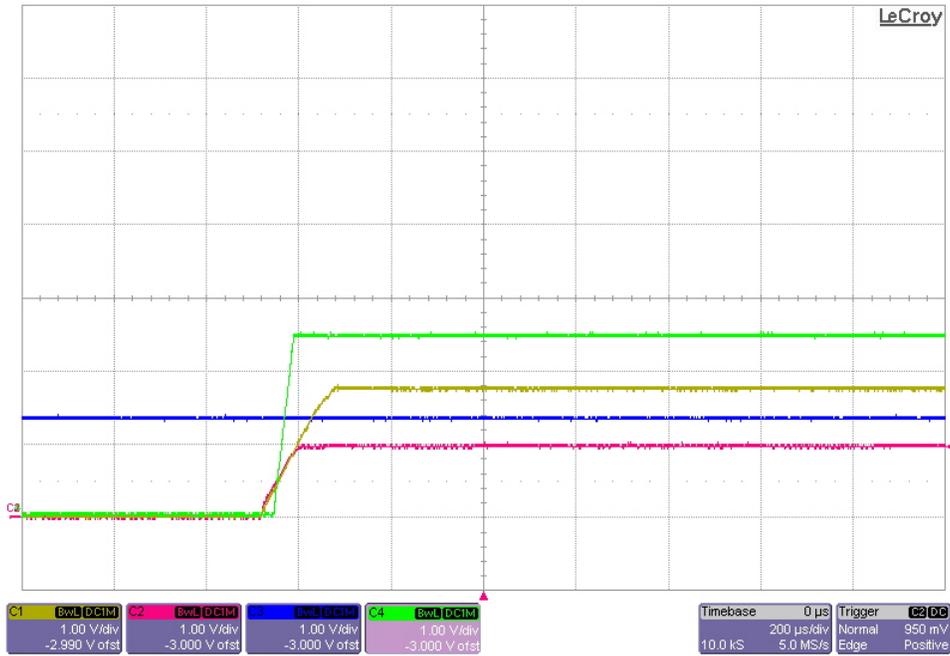


The 2.5V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 0.07A and the input voltage set to 3.3V. (10mV/DIV, 500nS/DIV)

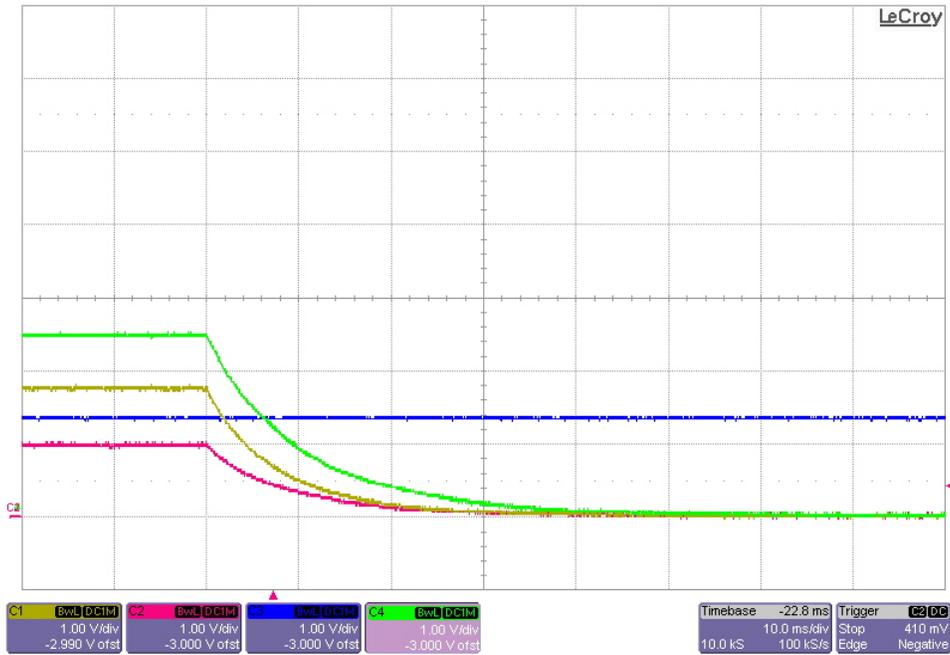


4 FET ENABLE (Power Save)

The output voltages are shown in the figure below as the FET_EN control changes from 5V to open. The outputs were unloaded and the input voltage was 5V. (1V/DIV, 200uS/DIV)

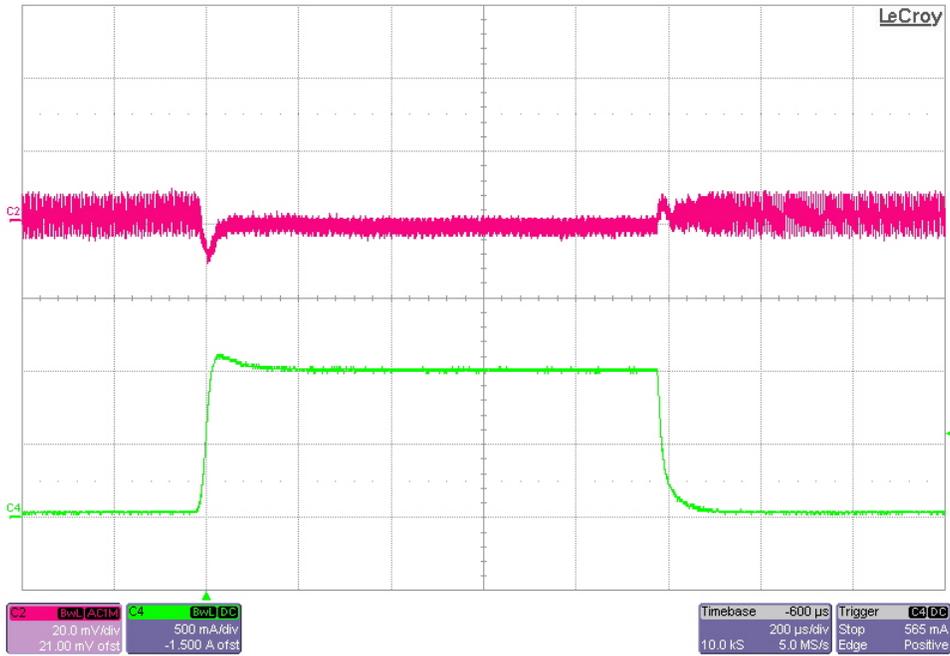


The output voltages are shown in the figure below as the FET_EN control changes from open to 5V. The outputs were unloaded and the input voltage was 5V. (1V/DIV, 200uS/DIV)

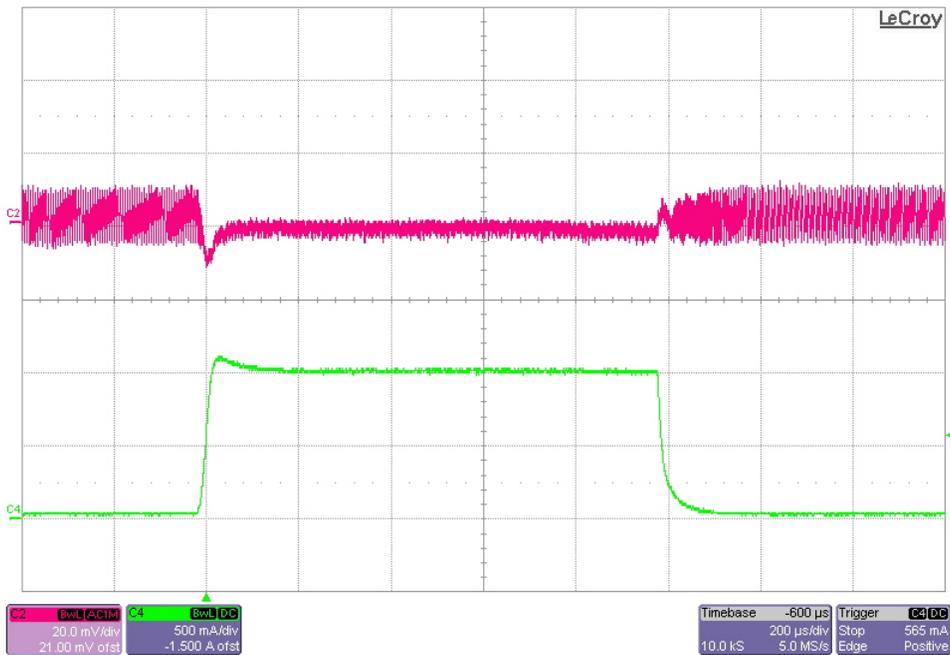


5 Load Transients

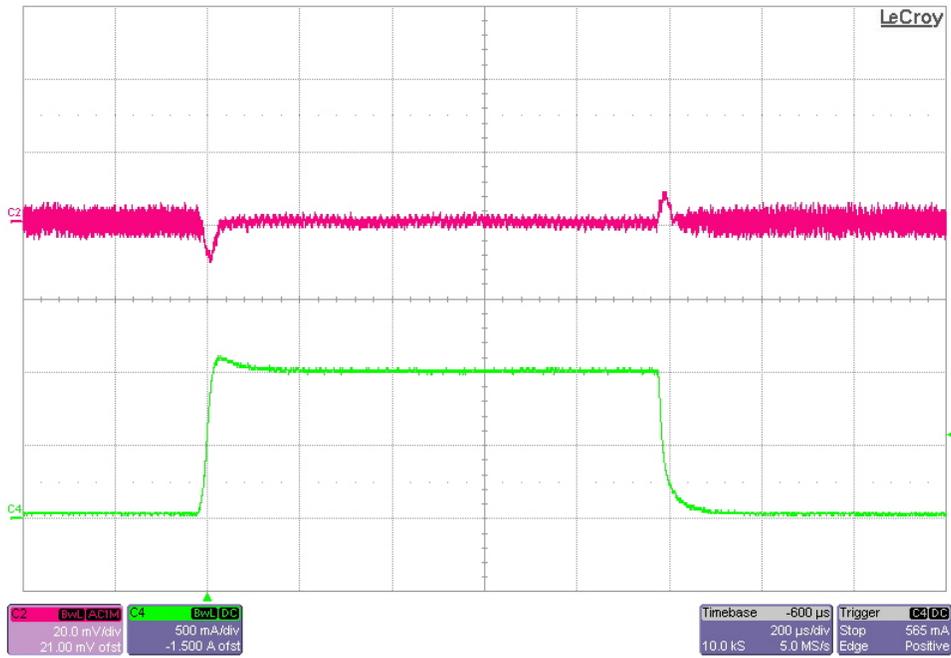
The photo below shows the 1V output voltage (ac coupled) when the load current is stepped between 0A and 1A. $V_{in} = 3.3V$. (20mV/DIV, 500mA/DIV, 200uS/DIV)



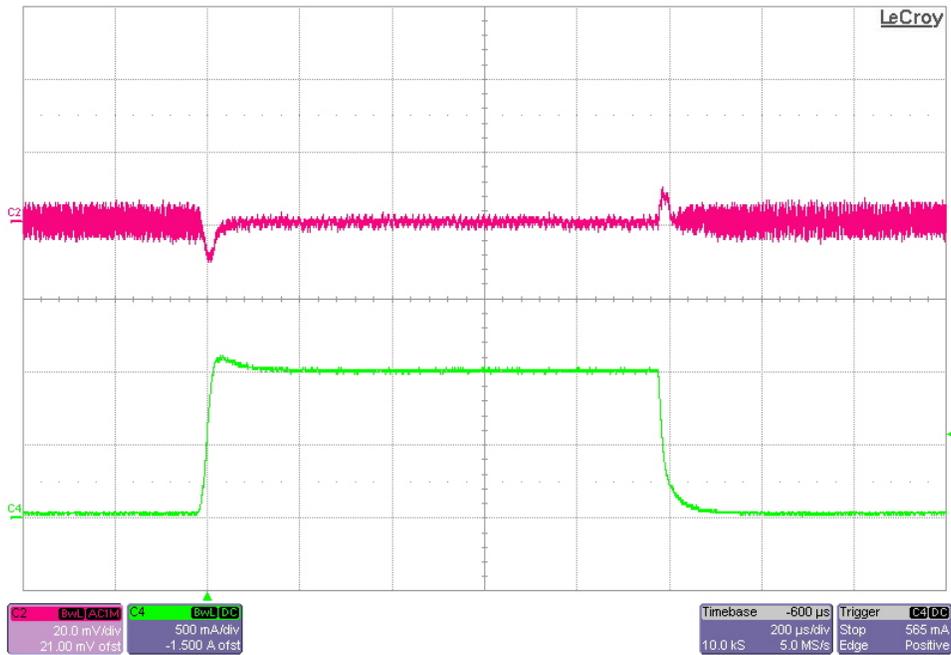
The photo below shows the 1V output voltage (ac coupled) when the load current is stepped between 0A and 1A. $V_{in} = 5V$. (20mV/DIV, 500mA/DIV, 200uS/DIV)



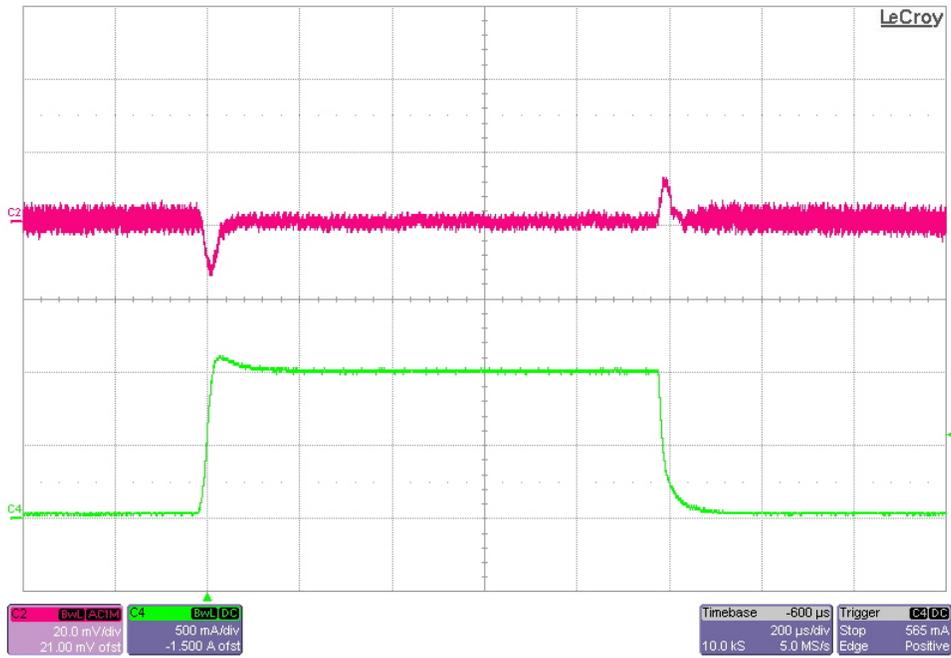
The photo below shows the 1.35V output voltage (ac coupled) when the load current is stepped between 0A and 1A. $V_{in} = 3.3V$. (20mV/DIV, 500mA/DIV, 200uS/DIV)



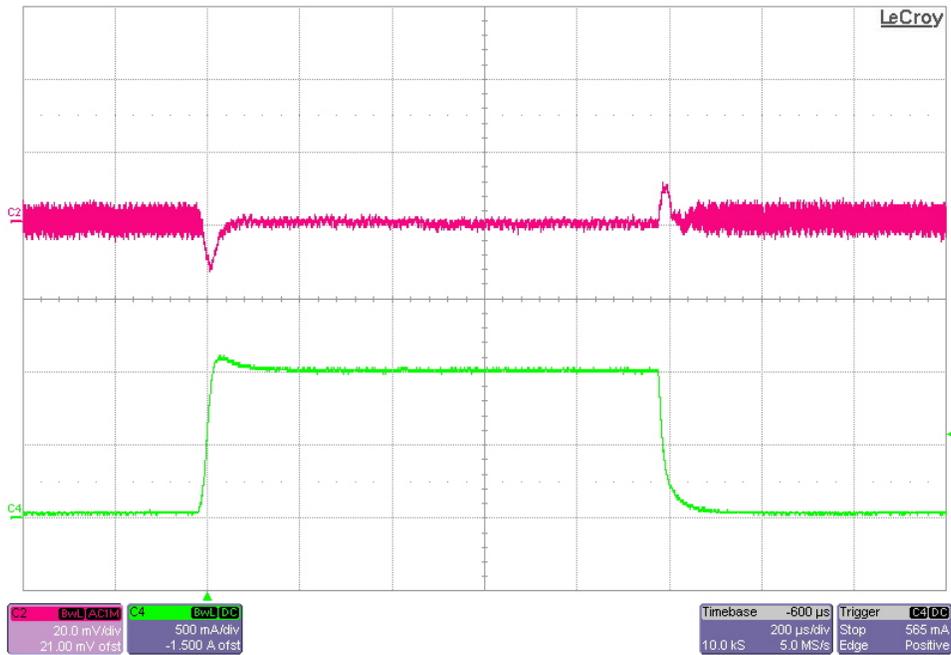
The photo below shows the 1.35V output voltage (ac coupled) when the load current is stepped between 0A and 1A. $V_{in} = 5V$. (20mV/DIV, 500mA/DIV, 200uS/DIV)



The photo below shows the 1.8V output voltage (ac coupled) when the load current is stepped between 0A and 1A. $V_{in} = 3.3V$. (20mV/DIV, 500mA/DIV, 200uS/DIV)

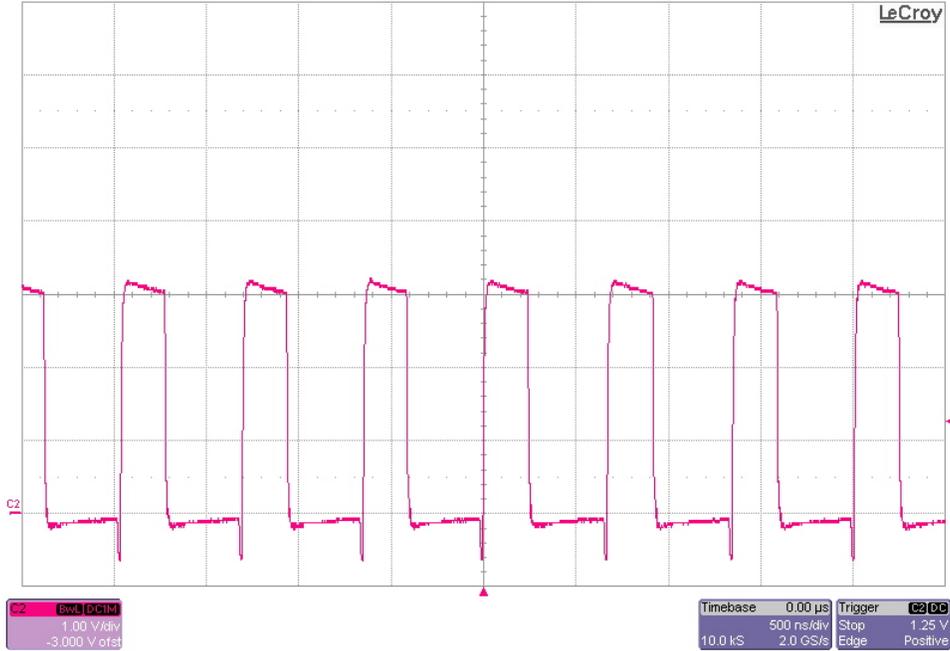


The photo below shows the 1.8V output voltage (ac coupled) when the load current is stepped between 0A and 1A. $V_{in} = 5V$. (20mV/DIV, 500mA/DIV, 200uS/DIV)

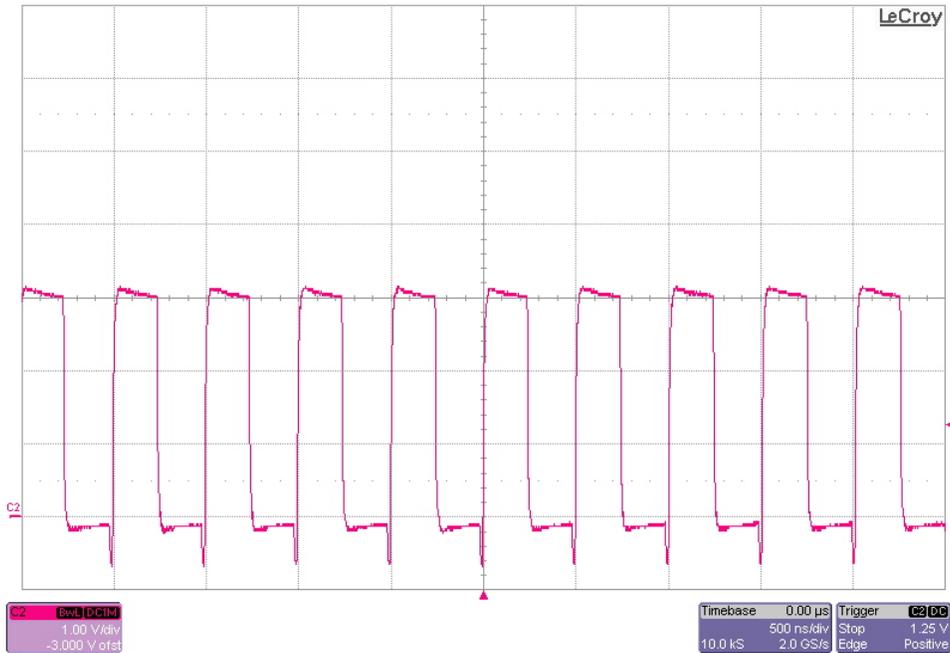


6 Switch Node Waveforms

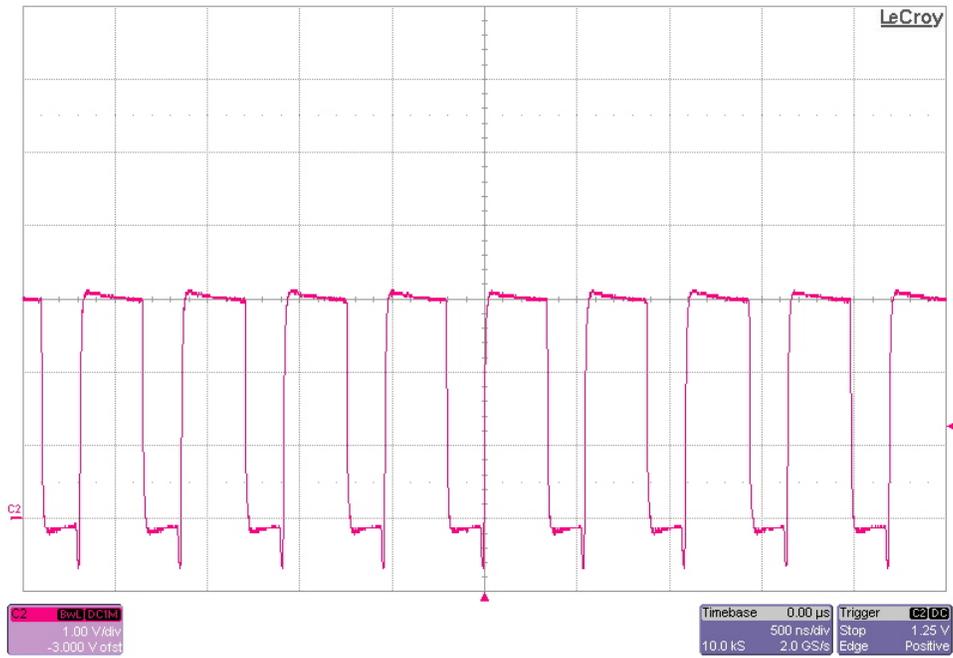
The photo below shows the 1V switch node. The input voltage is 3.3V and the output is loaded to 1A. (1V/DIV, 500nS/DIV)



The photo below shows the 1.35V switch node. The input voltage is 3.3V and the output is loaded to 1A. (1V/DIV, 500nS/DIV)

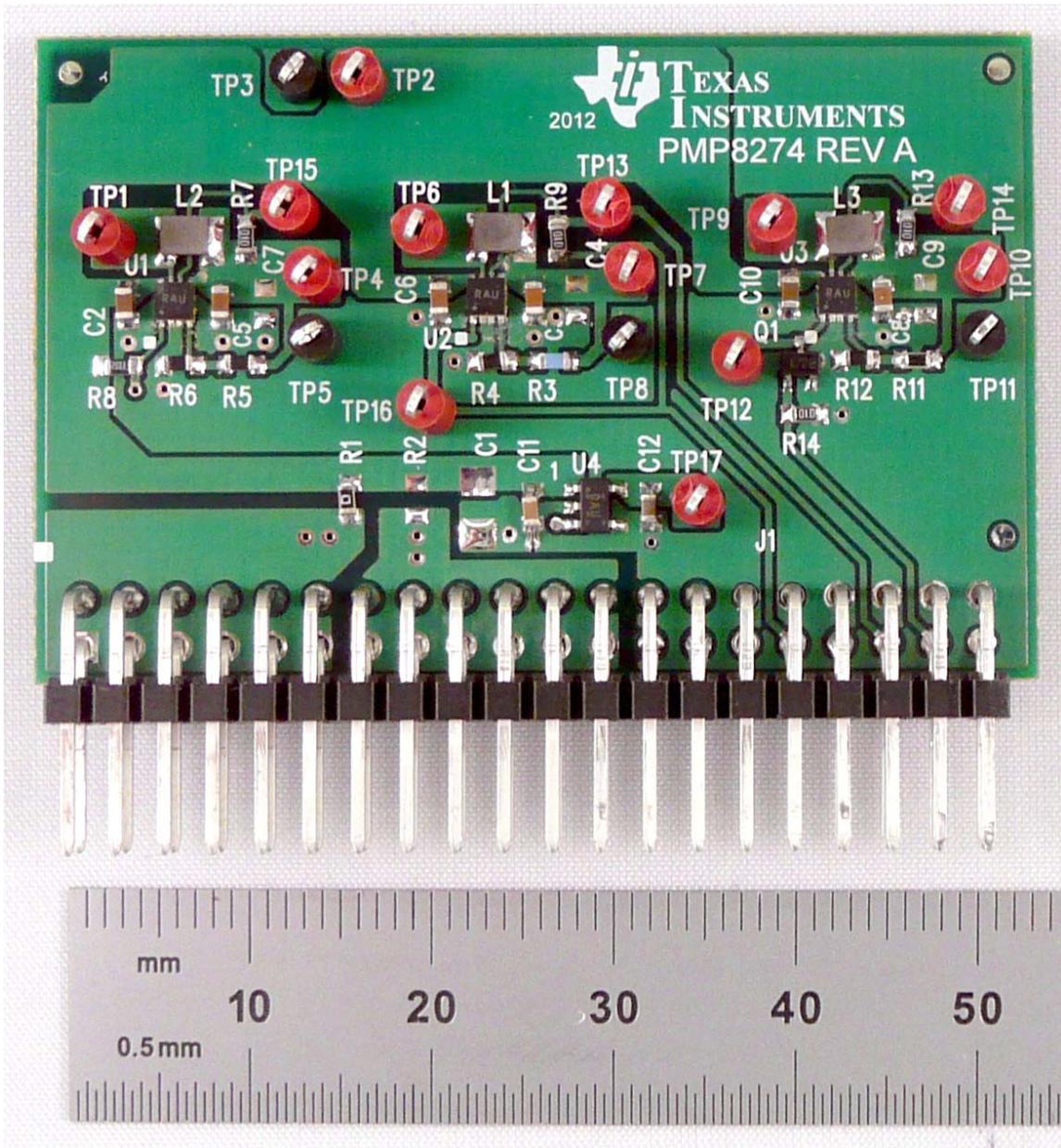


The photo below shows the 1.8V switch node. The input voltage is 3.3V and the output is loaded to 1A.
(1V/DIV, 500nS/DIV)



7 Photo

The photo below shows the PMP8274 REVA assy.



IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (<https://www.ti.com/legal/termsofsale.html>) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2021, Texas Instruments Incorporated