

XDS110 EnergyTrace™ Technology

The LP-XDS110ET features the EnergyTrace technology.

EnergyTrace implements a new method for measuring MCU current consumption. It uses a DC-DC solution to measure the time density of charge pulses, allowing accuracy on ultra low power measurements. Its high dynamic range (700 nA to 400 mA) and fast sampling rate (256 kSPS) captures the complete operational profile of the wireless MCU.

EnergyTrace Profile

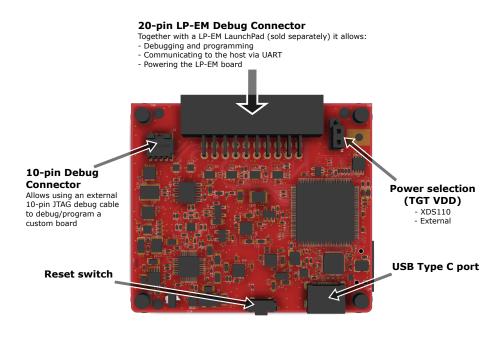
EnergyTrace Profile runtime and energy data for low power modes along with each function run during Active Mode.

Graphical Power Data in Code Composer Studio™

These two tabs of the EnergyTrace Technology window show a graph over time of power and energy.

> Find more information at ti.com/EnergyTrace

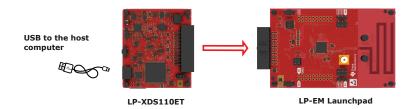
LP-XDS110ET Overview



Hardware setup

To use your new LP-XDS110ET Debug Probe with an LP-EM LaunchPad (sold separately), you need to connect the 20-pin LP-EM Debug connector to the corresponding header on the LP-EM LaunchPad.

Simply connect the edge connector of the Debug Probe to the edge connector of the LaunchPad and connect the USB port of the Debug Probe to the host computer. A secondary UART communications channel will also be available and power to the LaunchPad will be provided directly.



To use the 10-pin Debug Connector to debug a custom board, you need to purchase the cables separately to allow debugging/programming, UART communications and power. For additional details, consult dev.ti.com/LP-XDS110ET

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