

Symbol	Count	Hole Size	Plated	Drill Layer Pair	Hole Tolerance (+)	Hole Tolerance (-)	Hole Length
▽	422	8.00mil (0.203mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
⌘	1	8.00mil (0.203mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
⊕	4	31.50mil (0.800mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
□	2	33.07mil (0.840mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
✱	6	39.37mil (1.000mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
▽	262	40.16mil (1.020mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
▣	3	42.91mil (1.090mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
✱	2	62.99mil (1.600mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	23.62mil (0.600mm)
✱	2	82.68mil (2.100mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	23.62mil (0.600mm)
✱	4	157.48mil (4.000mm)	PTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	70.87mil (1.800mm)
⊙	24	32.00mil (0.813mm)	NPTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
◇	2	40.16mil (1.020mm)	NPTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
○	4	50.00mil (1.270mm)	NPTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
⊙	4	126.00mil (3.200mm)	NPTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
⊗	6	250.00mil (6.350mm)	NPTH	Top Layer - Bottom Layer	3.00mil (0.076mm)	3.00mil (0.076mm)	-
748 Total							

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	1.00mil	3.5	
1	Top Layer		1.40mil		
	Dielectric 2	FR-4	3.70mil	4.1	
2	GND01		1.38mil		
	Dielectric 1	FR-4	48.00mil	4.8	
3	PWR/GND02		1.38mil		
	Dielectric 3	FR-4	3.70mil	4.1	
4	Bottom Layer		1.40mil		
	Bottom Solder	Solder Resist	1.00mil	3.5	
	Bottom Overlay				

IMPEDENCE TABLE :

LAYER	TRACE WIDTH	SPACING	IMPEDANCE +/- 10%	REFERENCE LAYER
TOP	4 MILS	5 MILS	120OHM	LAYER-3
TOPIBOT	4 MILS	-	50 OHM	LAYER-2 /LAYER-3

199.61mm

95.62mm

0.00mm

1000.00mil

DESIGN INFORMATION

MIN. TRACK WIDTH: 4 MIL
MIN. CLEARANCE: 4 MIL
MIN. VIA PAD SIZE: 18 MIL
MINIMUM ANNULAR RING 0.12mm (5MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL

MATERIAL:
☐ FR-408 ☒ FR-4 High Tg ☐ OTHER
THICKNESS: ☒ 62 MIL (1.6mm) +/-10% ☐ OTHER
TOLERANCE: ☒ ANSI IPC-6012 TYPE 3 CLASS 2
☐ OTHER +/-
BOW & TWIST: ☒ ANSI IPC-6012 TYPE 3 CLASS 2
☐ OTHER +/-

DRILLING:
REFERENCE: ☒ AS SHOWN ☒ NC_DRILL FILES
PTH COPPER THICKNESS: ☒ 20-30 um ☐ OTHER

BOARD FINISH:
SILKSCREEN: ☒ TOP ☒ BOTTOM
SILKSCREEN COLOR: ☒ WHITE ☐ OTHER
SOLDER RESIST COLOR: ☒ GREEN ☐ OTHER
☐ MATTE ☒ SEMI-GLOSS

SURFACE FINISH: ☒ IMMERSION GOLD (ENIG) ☐ ENEPIG
☐ IMM. TIN/SILVER OR EQUIV ☐ OTHER

ARRAY/PANEL: ☐ CUT AND TRIM PER M1 BOARD OUTLINE
☐ N.C. ROUTE ☒ Breakaway Tabs

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:
☒ ANSI IPC-A-600F CLASS -> ☐ 1 ☒ 2 ☐ 3
☒ RoHS ☐ OTHER PER ORDER

ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER

ADDITIONAL REQUIREMENTS:
MICROSECTION: ☐ YES
BARE BOARD ELEC. TEST: ☐ NONE ☒ REQUIRED ☐ PER ORDER
☒ 8MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE
☐ XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE
☐ OUTER XX MIL TRACES REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE
☐ LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE

TEXAS INSTRUMENTS

PROJECT TITLE:
TMDSHSECDOCK-AM263

DESIGNED FOR:
Public Release

FILE NAME:
PROC148A_BRD.PcbDoc

ENGINEER:
Brennan Hartigan

LAYOUT BY:
Mistral

SCALE: 1.00

ALTUM DESIGNER VERSION:
22.8.2.66

ALL ARTWORK VIEWED FROM TOP SIDE

BOARD #: PROC148

REV: A

SUN REV: Not in version control

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LAYER NAME = M2056-0000

TID #: N/A

PLOT NAME = Fabrication Drawing

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