

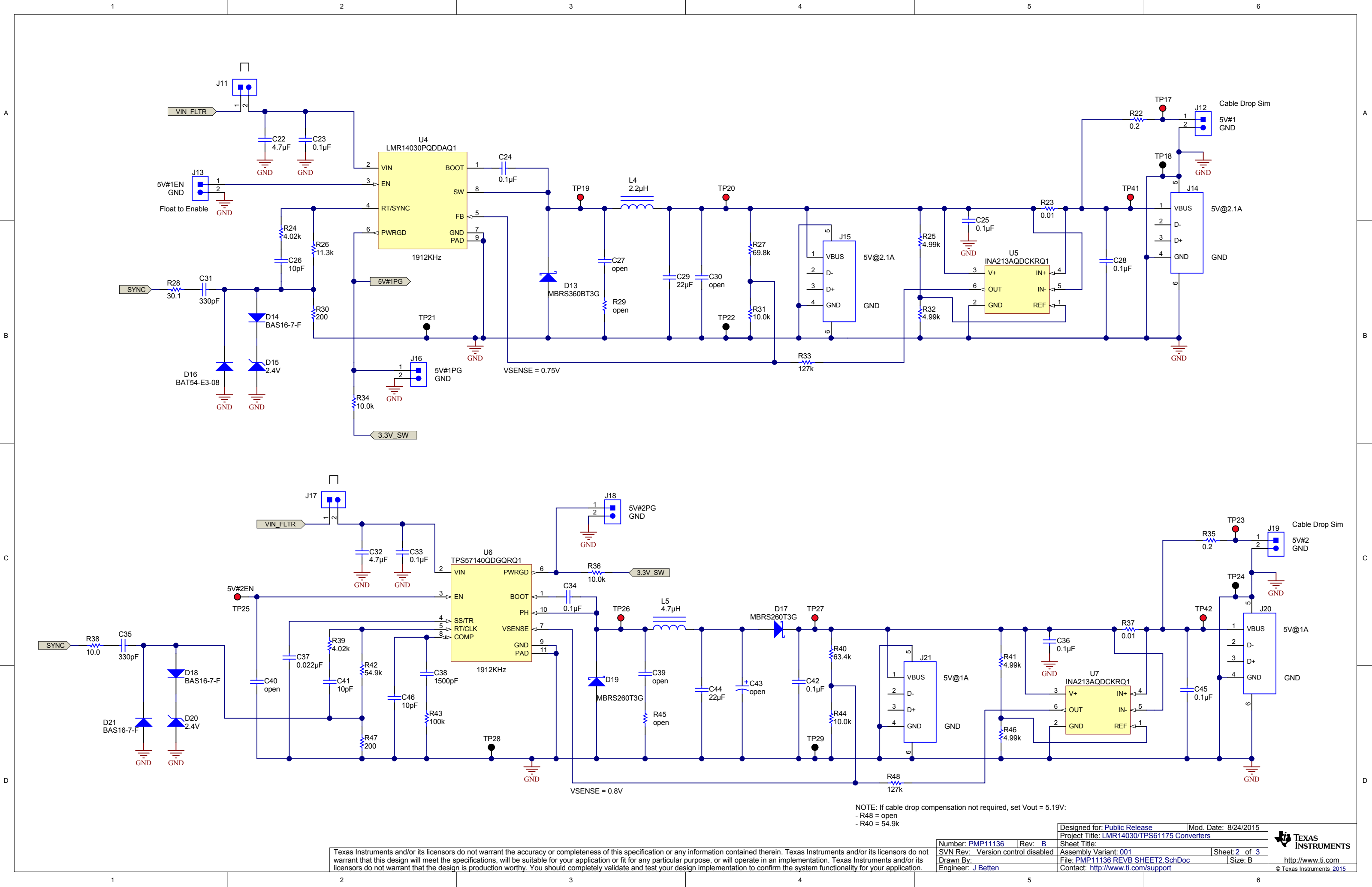
NOTES:  
1) Preliminary design only.

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Number: PMP11136  
Rev: B  
SVN Rev: Version control disabled  
Drawn By:  
Engineer: J Betten

Designed for: Public Release  
Project Title: LMR14030/TPS61175 Converters  
Sheet Title:  
Assembly Variant: 001  
File: PMP11136 REVB SHEET1.SchDoc  
Contact: <http://www.ti.com/support>

Mod. Date: 8/24/2015  
Sheet: 1 of 3  
Size: B  
© Texas Instruments 2015



NOTE: If cable drop compensation not required, set Vout = 5.19V:  
- R48 = open  
- R40 = 54.9k

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Number: PMP11136 Rev: B  
SVN Rev: Version control disabled  
Drawn By:  
Engineer: J Betten

Designed for: Public Release Mod. Date: 8/24/2015  
Project Title: LMR14030/TPS61175 Converters  
Sheet Title:  
Assembly Variant: 001 Sheet: 2 of 3  
File: PMP11136 REV B SHEET2.SchDoc Size: B  
Contact: <http://www.ti.com/support>



© Texas Instruments 2015

A

B

C

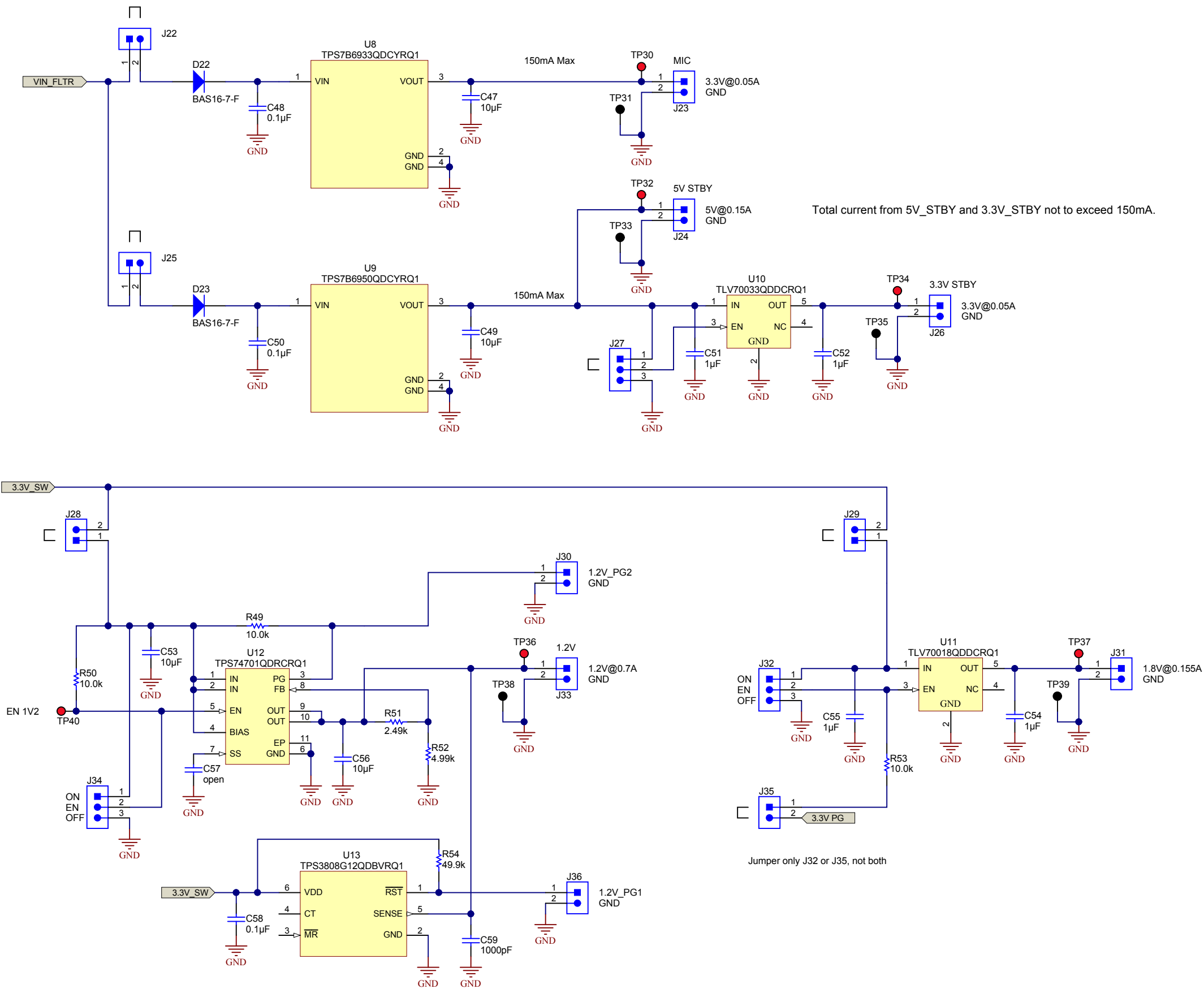
D

A

B

C

D



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Number: PMP11136 Rev: B  
SVN Rev: Version control disabled  
Drawn By:  
Engineer: J Betten

Designed for: Public Release	Mod. Date: 8/24/2015
Project Title: LMR14030/TPS61175 Converters	
Sheet Title:	
Assembly Variant: 001	Sheet: 3 of 3
File: PMP11136 REVB SHEET3.SchDoc	Size: B
Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	